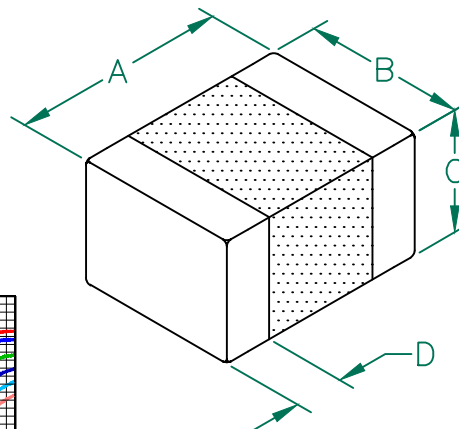


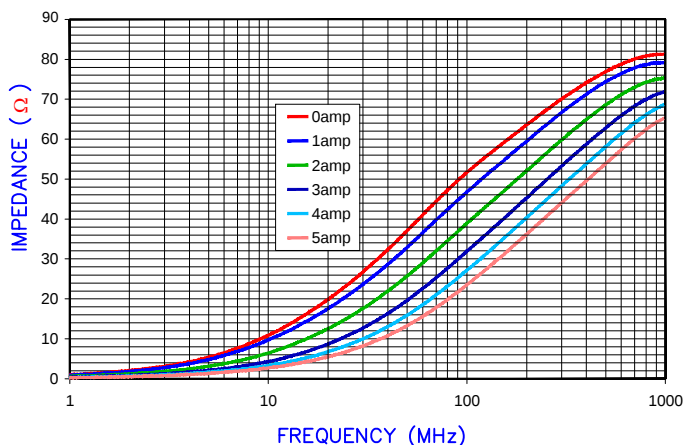
DM1612X560R-10

PHYSICAL DIMENSIONS:

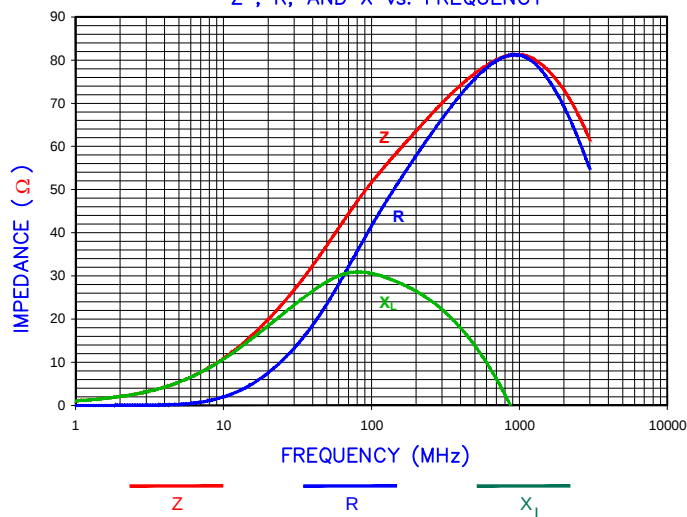
A	4.06 [.160]	+ 0.20 [.008]
B	3.05 [.120]	+ 0.20 [.008]
C	2.28 [.090]	+ 0.20 [.008]
D	0.70 [.028]	+ 0.25 [.010]



Z vs FREQUENCY
IMPEDANCE UNDER DC BIAS

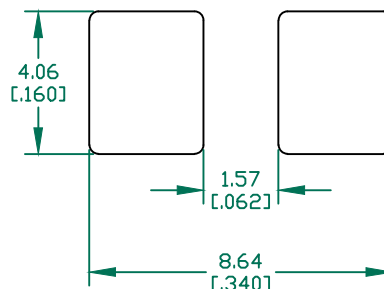


Z, R, AND X vs. FREQUENCY



AGILENT E4991A RF Impedance/Material Analyzer
HP 16194A Test Fixture. TEST REF. 3236

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762
[.030] to this dimension.)

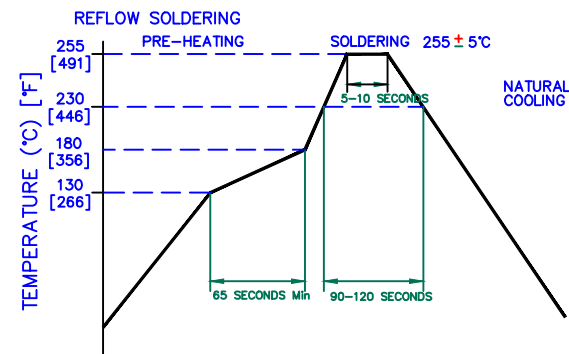
ELECTRICAL CHARACTERISTICS:

Z @ 100MHz (Ω)	DCR (Ω)	Rated Current
Nominal	56	
Minimum	42	
Maximum	70	10,000 mA

NOTES: UNLESS OTHERWISE SPECIFIED

1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 13" REELS, 2,500 PCS/REEL.
2. TERMINATION FINISH IS 100% TIN.
3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
4. OPERATING TEMPERATURE TEMP: $-40^{\circ}\text{C} \sim +125^{\circ}\text{C}$ (INCLUDING SELF-HEATING)

RECOMMENDED SOLDERING CONDITIONS



RoHS

DIMENSIONS ARE IN mm [INCHES].				This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.			
G	OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	PROJECT/PART NUMBER: DM1612X560R-10			
F	UPDATE D DIMENSION	03/12/10	JUN				
E	UPDATE COMPANY LOGO	05/22/09	JRK	DATE: 04/02/04 SCALE: NTS SHEET: 1 of 1			
D	UPDATE COMPANY LOGO	1/11/08	JRK				
C	CHG TOLS ON D DIMENSION	08/28/06	JRK	TOOL # -			
B	CHANGE D DIMENSION & TOLERANCE	03/21/06	JRK				
A	ORIGINAL DRAFT	04/02/04	TMB	CAD # DM1612X560R-10-G			
REV	DESCRIPTION	DATE	INT				

Laird

REV G PART TYPE: CO-FIRE DRAWN BY: TMB

Mouser Electronics

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