

# Dual Power Operational Amplifiers

## FEATURES

- RoHS COMPLIANT
- LOW COST
- WIDE BANDWIDTH - 1.1 Mhz
- HIGH OUTPUT CURRENT - 2.5A (Combined)
- WIDE COMMON MODE RANGE Includes negative supply
- WIDE SUPPLY VOLTAGE RANGE Single supply: 5V to 40V  
Split supplies:  $\pm 2.5V$  to  $\pm 20V$
- LOW QUIESCIENT CURRENT
- VERY LOW DISTORTION

## APPLICATIONS

- HALF AND FULL BRIDGE MOTOR DRIVERS
- AUDIO POWER AMPLIFIER
- IDEAL FOR SINGLE SUPPLY SYSTEMS  
5V Peripherals, 12V Automotive, 28V Avionic

## PACKAGING OPTIONS

- 7 TO-220 Plastic Package (PA75CD)
- 7 TO-220 with Staggered Lead Form (PA75CX)
- 7 DDPak Surface Mount Package (PA75CC)

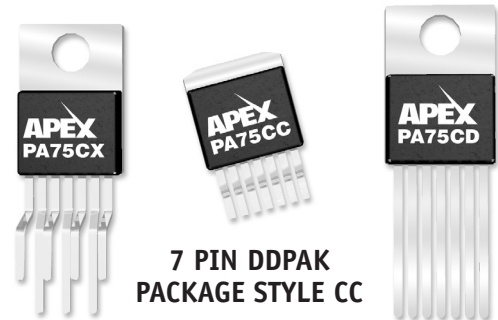
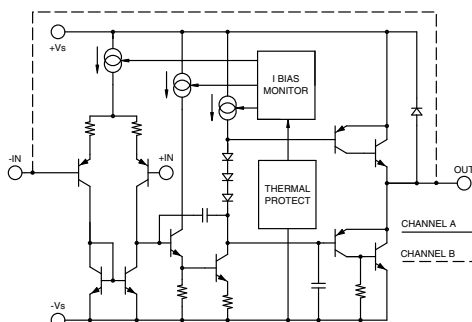
## DESCRIPTION

The amplifier design consists of dual power op amp on a single monolithic die. Side B of the dual monolithic is configured as a unity gain buffer to increase the current capability of the master side A. The use of two PA75 amplifiers provides a cost-effective solution to applications where multiple amplifiers are required or a bridge configuration is needed. Very low harmonic distortion of .02% THD and low  $I_Q$  makes the PA75 a good solution for power audio applications.

The PA75 is available in three standard package designs. The surface mount version of the PA75, the PA75CC, is an industry standard non-hermetic plastic 7-pin DDPak. The through hole version of the PA75, the PA75CD and PA75CX, are industry standard non-hermetic plastic 7-pin TO-220 packages. The PA75CX is staggered lead formed and offers standard 100 mil spacing. This allows for easier PC board layout. (Please refer to the CX lead form package drawing for dimension of the PA75CX).

The monolithic amplifier is directly attached to the metal tabs of the PA75CC, PA75CD, and PA75CX. The metal tabs of the packages are directly tied to  $-V_s$ .

## EQUIVALENT SCHEMATIC ONE CHANNEL

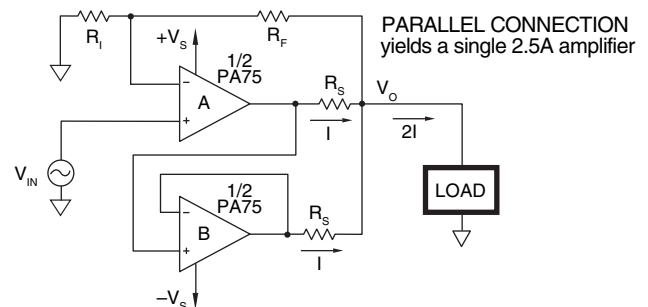


7 PIN TO220  
STAGGERED LEADS  
PACKAGE STYLE CX

7 PIN TO220  
PACKAGE STYLE CD

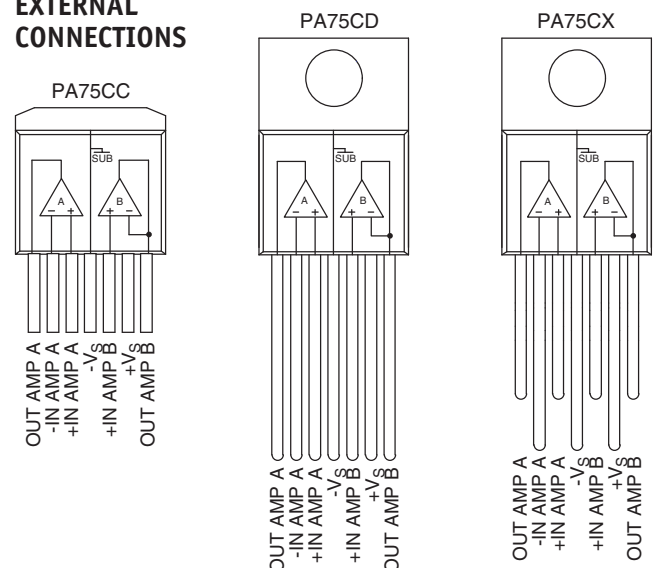
## TYPICAL APPLICATION

Ref: APPLICATION NOTES 8, 20, 26



Combining the power op amp (master channel A) and the unity gain buffer (slave channel B) in a parallel connection yields a single 2.5A amplifier.  $R_I$  and  $R_F$  can set up channel A for the required gain for the overall circuit. Small values of  $R_S$  (sense resistors) are used on the outputs to improve current sharing characteristics. The master amplifier can be configured in inverting or non-inverting gain configurations.

## EXTERNAL CONNECTIONS



**ABSOLUTE MAXIMUM RATINGS**

|                                               |                   |
|-----------------------------------------------|-------------------|
| SUPPLY VOLTAGE, total                         | 5V to 40V         |
| OUTPUT CURRENT                                | SOA               |
| POWER DISSIPATION, internal, (per amplifier)  | 19.5W             |
| POWER DISSIPATION, internal (both amplifiers) | 28.6W             |
| INPUT VOLTAGE, differential                   | $\pm V_S$         |
| INPUT VOLTAGE, common mode                    | $+V_S, -V_S-0.5V$ |
| JUNCTION TEMPERATURE, max <sup>1</sup>        | 150°C             |
| TEMPERATURE, pin solder—10 sec max            | 220°C             |
| TEMPERATURE RANGE, storage                    | -55°C to 150°C    |
| OPERATING TEMPERATURE RANGE, case             | -40°C to 125°C    |

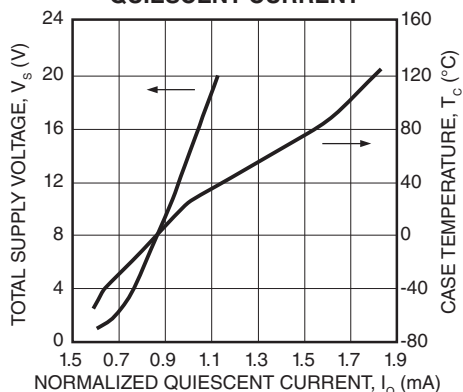
**SPECIFICATIONS**

| PARAMETER                                    | TEST CONDITIONS <sup>2</sup>                          | MIN           | TYP           | MAX        | UNITS             |
|----------------------------------------------|-------------------------------------------------------|---------------|---------------|------------|-------------------|
| <b>INPUT</b>                                 |                                                       |               |               |            |                   |
| OFFSET VOLTAGE, initial                      |                                                       |               | 1             | 15         | mV                |
| OFFSET VOLTAGE, vs. temperature              | Full temperature range                                |               | 20            |            | $\mu V/^{\circ}C$ |
| BIAS CURRENT, initial                        |                                                       |               | 100           | 500        | nA                |
| COMMON MODE RANGE                            | Full temperature range                                | $-V_S$        |               | $+V_S-1.3$ | V                 |
| COMMON MODE REJECTION, DC                    | Full temperature range                                | 60            | 90            |            | dB                |
| POWER SUPPLY REJECTION                       | Full temperature range                                | 60            | 90            |            | dB                |
| CHANNEL SEPARATION                           | $I_{OUT} = 500mA, f = 1kHz$                           | 50            | 68            |            | dB                |
| INPUT NOISE VOLTAGE                          | $R_S = 100\Omega, f = 1$ to 100kHz                    |               | 22            |            | $nV/\sqrt{Hz}$    |
| <b>GAIN</b>                                  |                                                       |               |               |            |                   |
| OPEN LOOP GAIN                               | Full temperature range                                | 89            | 100           |            | dB                |
| GAIN BANDWIDTH PRODUCT                       | $A_V = 40dB$                                          | 0.9           | 1.4           |            | MHz               |
| PHASE MARGIN                                 | Full temperature range, $R_L = 2K\Omega, C_L = 100pF$ |               | 65            |            | °                 |
| POWER BANDWIDTH                              | $V_{O(P-P)} = 28V$                                    |               | 13.6          |            | kHz               |
| <b>OUTPUT</b>                                |                                                       |               |               |            |                   |
| CURRENT, peak                                |                                                       |               |               | 1.5        | A                 |
| SLEW RATE                                    |                                                       | 1             | 1.4           |            | V/ $\mu s$        |
| VOLTAGE SWING                                | Full Temperature Range, $I_O = 100mA$                 | $ V_S  - 1.1$ | $ V_S  - .8$  |            | V                 |
| VOLTAGE SWING                                | Full Temperature Range, $I_O = 1A$                    | $ V_S  - 1.8$ | $ V_S  - 1.4$ |            | V                 |
| HARMONIC DISTORTION                          | $A_V = 1, R_2 = 50\Omega, V_O = .5V_{RMS}, f = 1kHz$  |               | .02           |            | %                 |
| <b>POWER SUPPLY</b>                          |                                                       |               |               |            |                   |
| VOLTAGE, $V_{SS}$ <sup>3</sup>               |                                                       | 5             | 30            | 40         | V                 |
| CURRENT, quiescent, total                    |                                                       |               | 8             | 10         | mA                |
| <b>THERMAL</b>                               |                                                       |               |               |            |                   |
| RESISTANCE,DC junction to case (single)      |                                                       |               | 5.84          | 6.42       | $^{\circ}C/W$     |
| RESISTANCE,AC junction to case (single)      |                                                       |               | 4.38          | 4.81       | $^{\circ}C/W$     |
| RESISTANCE,DC junction to case (both)        |                                                       |               | 3.97          | 4.36       | $^{\circ}C/W$     |
| RESISTANCE,AC junction to case (both)        |                                                       |               | 2.98          | 3.27       | $^{\circ}C/W$     |
| RESISTANCE,junction to air (CD,CX)           |                                                       |               | 60            |            | $^{\circ}C/W$     |
| RESISTANCE,junction to air (CC) <sup>4</sup> |                                                       |               | 27            |            | $^{\circ}C/W$     |
| TEMPERATURE RANGE,case                       | Meets full range specifications                       | -25           |               | 85         | $^{\circ}C$       |

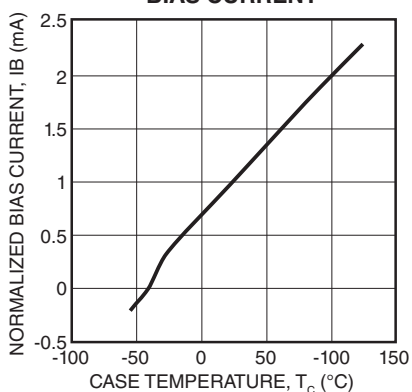
- NOTES: 1. Long term operation at the maximum junction temperature will result in reduced product life. Derate internal power dissipation to achieve high MTTF.
2. Unless otherwise noted, the following conditions apply:  $\pm V_S = \pm 15V, T_C = 25^{\circ}C$ .
3.  $+V_S$  and  $-V_S$  denote the positive and negative supply rail respectively.  $V_{SS}$  denotes the total rail-to-rail supply voltage.
4. Heat tab attached to 3/32" FR-4 board with 2oz. copper. Topside copper area (heat tab directly attached) = 1000 sq. mm, back-side copper area = 2500 sq. mm, board area = 2500 sq. mm.

## TYPICAL PERFORMANCE GRAPHS

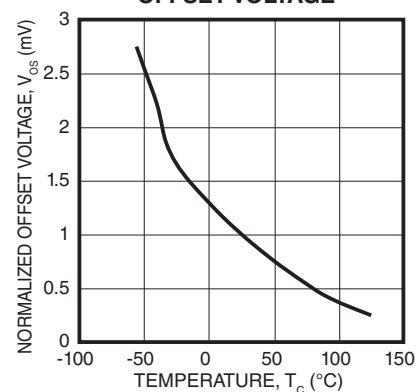
### QUIESCENT CURRENT



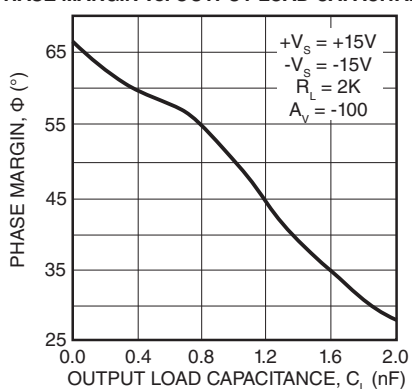
### BIAS CURRENT



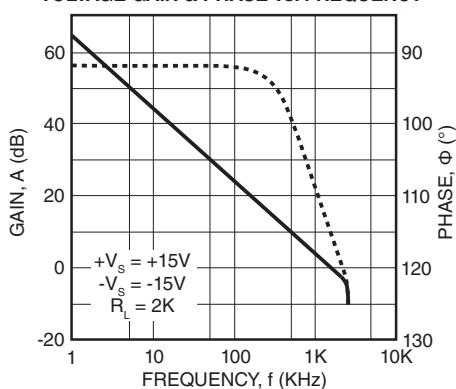
### OFFSET VOLTAGE



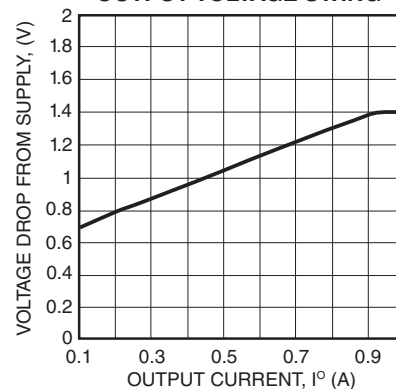
### PHASE MARGIN vs. OUTPUT LOAD CAPACITANCE



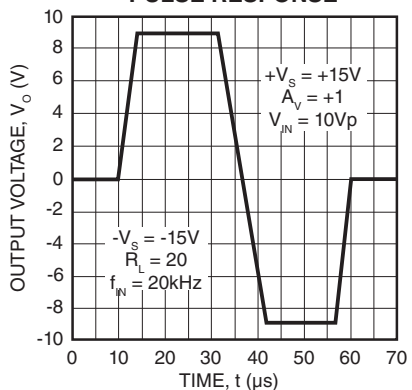
### VOLTAGE GAIN & PHASE vs. FREQUENCY



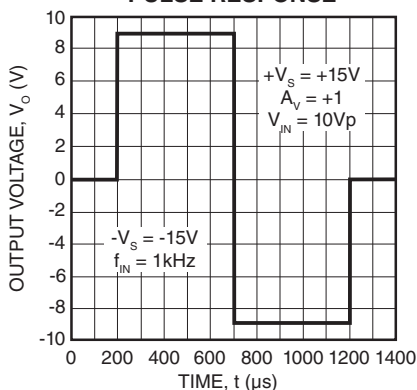
### OUTPUT VOLTAGE SWING



### PULSE RESPONSE



### PULSE RESPONSE

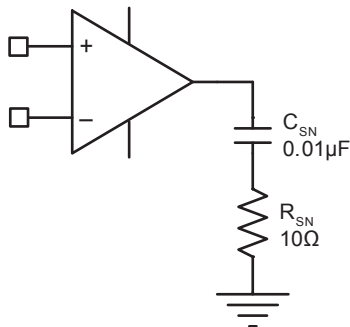


## GENERAL

Please read Application Note 1 "General Operating Considerations" which covers stability, supplies, heatsinking, mounting, SOA interpretation, and specification interpretation. Visit [www.Cirrus.com](http://www.Cirrus.com) for design tools that help automate tasks such as calculations for stability, internal power dissipation, heatsink selection; Apex Precision Power's complete Application Notes library; Technical Seminar Workbook; and Evaluation Kits.

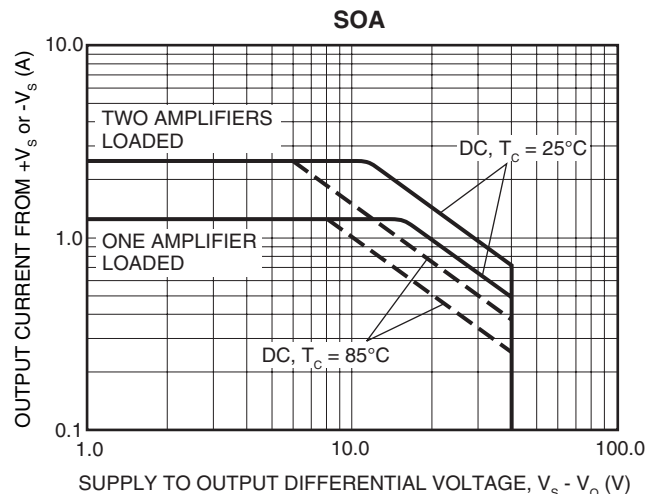
## STABILITY CONSIDERATIONS

All monolithic power op amps use output stage topologies that present special stability problems. This is primarily due to non-complementary (both devices are NPN) output stages with a mismatch in gain and phase response for different polarities of output current. It is difficult for the op amp manufacturer to optimize compensation for all operating conditions. For applications with load current exceeding 300ma, oscillation may appear. The oscillation may occur only with the output voltage swing at the negative or positive half cycle. Under most operating and load conditions acceptable stability can be achieved by providing a series RC snubber network connected from the output to ground. The recommended component values of the network are,  $R_{SN} = 10\Omega$  and  $C_{SN} = 0.01\mu F$ . Please refer to Application Note 1 for further details.



## SAFE OPERATING AREA (SOA)

The SOA curves combine the effect of all limits for this power op amp. For a given application, the direction and magnitude of the output current should be calculated or measured and checked against the SOA curves. This is simple for resistive loads but more complex for reactive and EMF generating loads.



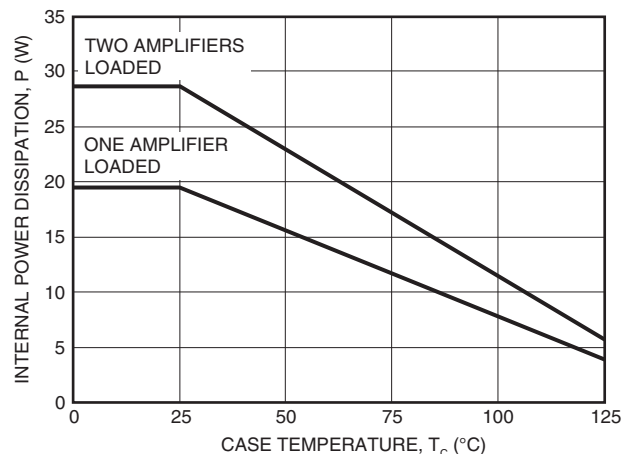
## THERMAL CONSIDERATIONS

The PA75CD and CX have a large exposed copper heat tab to which the monolithic is directly attached. The PA75CD and CX may require a thermal washer, which is electrically insulating since the tab is directly tied to  $-V_S$ . This can result in a thermal impedance RCS of up to  $1^\circ\text{C/W}$  or greater.

The PA75CC has a large exposed integrated copper heatslug to which the monolithic is directly attached. The solder connection of the heatslug to a minimum of 1 square inch foil area of the printed circuit board will result in thermal performance of  $25^\circ\text{C/W}$  junction to air rating of the PA75CC. Solder connection to an area of 1 to 2 square inches of foil is required for minimal power applications.

Where the PA75CC is used in higher power applications, it is necessary to use surface mount techniques of heatsinking. Surface mount techniques include the use of a surface mount fan in combination with a surface mount heatsink on the backside of the FR4/PC board with through hole thermal vias. Other highly thermal conductive substrate board materials are available for maximum heat sinking.

## POWER DERATING



## MOUNTING PRECAUTIONS

1. Always use a heat sink. Even unloaded the PA75 can dissipate up to .4 watts.
2. Avoid bending the leads. Such action can lead to internal damage.
3. Always fasten the tab of the CD and CX package to the heat sink before the leads are soldered to fixed terminals.
4. Strain relief must be provided if there is any probability of axial stress to the leads.



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## CONTACTING CIRRUS LOGIC SUPPORT

For all Apex Precision Power product questions and inquiries, call toll free 800-546-2739 in North America.

For inquiries via email, please contact [apex.support@cirrus.com](mailto:apex.support@cirrus.com).

International customers can also request support by contacting their local Cirrus Logic Sales Representative.

To find the one nearest to you, go to [www.cirrus.com](http://www.cirrus.com)

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