

**PI3USB302-A**

**3.3V, USB 3.0, 1-Port, 2:1 Mux/DeMux Switch**

**Features**

- 2 Differential Channel, 2:1 Mux/DeMux
- USB 3.0 SuperSpeed Switch
- Bi-directional Operation
- Low Bit-to-Bit Skew, 10 ps max
- 3 dB Bandwidth = 8.1 GHz
- Low channel-to-channel skew: 20 ps max
- Low insertion loss: -1 dB @ 2.5 GHz (5.0 Gbps)
- Low Crosstalk: -33 dB @ 2.5 GHz (5.0 Gbps)
- Low Off Isolation: -27 dB @ 2.5 GHz (5.0 Gbps)
- Low Return Loss: -23.3 dB @ 2.5GHz (5.0Gbps)
- V<sub>DD</sub> Operating Range: 3.3 V +/-10%
- ESD Tolerance: 2 kV HBM
- Low current: 0.2 mA Typ.
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. “Green” Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](https://www.diodes.com/quality/product-definitions/) or your local Diodes representative.  
<https://www.diodes.com/quality/product-definitions/>
- Packaging (Pb-free & Green):
  - 20-contact, 2.5 × 4.5mm TQFN (ZB)

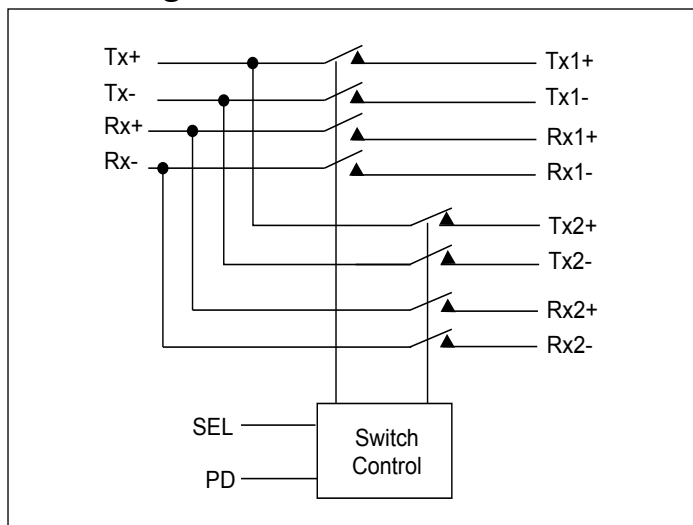
**Description**

Diodes' PI3USB302-A is a 2-differential channel bi-directional multiplexer/demultiplexer switch. Due to its low bit-to-bit skew, high channel-to-channel noise isolation and bandwidth, this product is ideal for USB 3.0 signal switching at 5.0 Gbps.

**Application**

Routing USB 3.0 SuperSpeed signals

**Block Diagram**



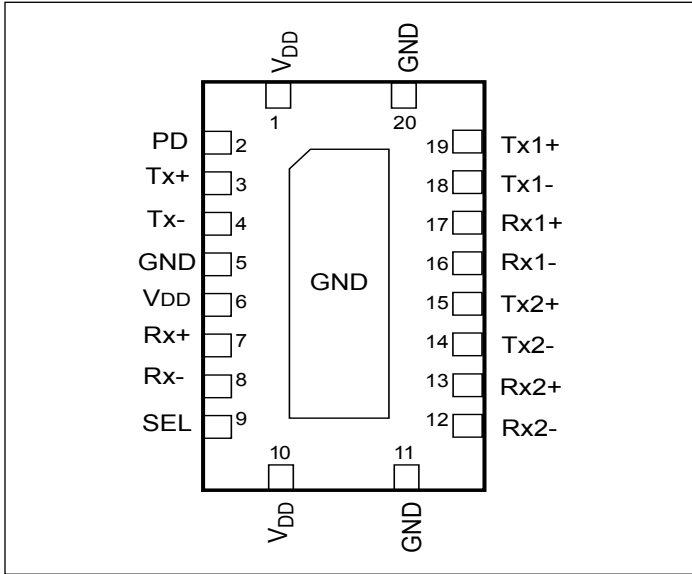
**Truth Table**

Function	SEL	PD
Port 1 is active	L	L
Port 2 is active	H	L
Both Ports Hi-z, IC power down	x	H

**Notes:**

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

**Pin Configuration (Top-side view)**



**Pin Description**

Pin #	Pin Name	I/O	Description
2	PD	I	Power Down input. see truth table on page 1 for functionality
3	Tx+	I/O	Signal I/O, Transmit differential pair from common port
4	Tx-	I/O	
7	Rx+	I/O	Signal I/O, Receive differential pair from common port
8	Rx-		
9	SEL	I	Operation mode Select (when SEL=0: port 1 is active, when SEL=1: port 2 is active)
14	Tx2-	I/O	Signal I/O, Transmit differential pair, port 2
15	Tx2+		
12	Rx2-	I/O	Signal I/O, Receive differential pair, port 2
13	Rx2+		
17	Rx1+	I/O	Signal I/O, Receive differential pair, port 1
16	Rx1-		
19	Tx1+	I/O	Signal I/O, Transmit differential pair, port 1
18	Tx1-		
6, 10, 11	V <sub>DD</sub>	Pwr	3.3V ±10% Positive Supply Voltage
5, 11, 20, Center Pad	GND	Pwr	Power ground

## Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature .....	-65°C to +150°C
Supply Voltage to Ground Potential .....	-0.5V to +4.6V
Channel DC Input Voltage .....	-0.5V to 1.5V
SEL/PD DC Input Voltage .....	-0.5V to 4.6V
DC Output Current .....	120mA
Power Dissipation .....	0.5W

**Note:**

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

## Electrical Characteristics

Recommended Operating Conditions

Symbol	Parameter	Conditions	Min	Typ	Max	Units
V <sub>DD</sub>	3.3V Power Supply		3.0	3.3	3.6	V
I <sub>DD</sub>	Total current from V <sub>DD</sub> 3.3V supply	SEL = 0V or V <sub>DD</sub> ,		0.2	1	mA
I <sub>DD_PD</sub>	Power down current	PD = 1		20	40	µA
T <sub>CASE</sub>	Case temperature range for operation within spec.		-40		85	°C
T <sub>j</sub>	Junction Temperature	Absolute			125	°C
		Operating			85	

## DC Electrical Characteristics for Switching over Operating Range

Parameters	Description	Test Conditions <sup>(1)</sup>	Min	Typ <sup>(1)</sup>	Max	Units
V <sub>IH-SEL, PD</sub>	Input HIGH Voltage, SEL, PD Input		2		3.6	V
V <sub>IL-SEL, PD</sub>	Input LOW Voltage, SEL, PD Input		0		0.8	
V <sub>IK</sub>	Clamp Diode Voltage	V <sub>DD</sub> = Max, I <sub>IN</sub> = -18mA		-0.7	-1.2	
I <sub>IH</sub>	Input HIGH Current SEL, PD Input	V <sub>DD</sub> = Max, V <sub>IN</sub> = V <sub>DD</sub>	-5		+5	µA
I <sub>IL</sub>	Input LOW Current, SEL, PD Input	V <sub>DD</sub> = Max, V <sub>IN</sub> = 0V	-5		+5	
I <sub>IH</sub>	Input High Current, T <sub>X</sub> , R <sub>X</sub>	V <sub>DD</sub> = Max, V <sub>IN</sub> = 1.5V	-10		+10	
I <sub>IL</sub>	Input LOW Current, T <sub>X</sub> , R <sub>X</sub>	V <sub>DD</sub> = Max, V <sub>IN</sub> = 0V	-10		+10	
I <sub>OZH</sub>	High Z HIGH Current, T <sub>X</sub> , R <sub>X</sub>	V <sub>DD</sub> = Max, V <sub>IN</sub> = 1.5V	-10		+10	µA
I <sub>OZL</sub>	High Z LOW Current, T <sub>X</sub> , R <sub>X</sub>	V <sub>DD</sub> = Max, V <sub>IN</sub> = 0V	-10		+10	µA

**Note:**

1. Typical values are at V<sub>DD</sub> = 3.3V, T<sub>A</sub> = 25°C ambient and maximum loading.

## Switching Characteristics

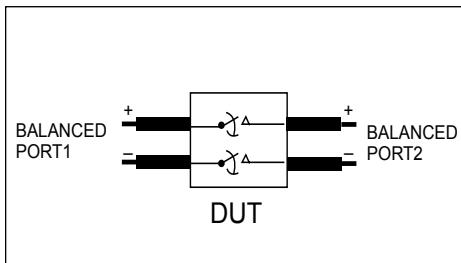
Parameters	Description	Test Conditions	Min.	Typ.	Max.	Units
t <sub>PZH</sub> , t <sub>PZL</sub>	Line Enable Time			25	30	ns
t <sub>PHZ</sub> , t <sub>PLZ</sub>	Line Disable Time			5	25	
t <sub>b-b</sub>	Bit-to-bit skew within the same differential pair			5	10	ps
t <sub>ch-ch</sub>	Channel-to-channel skew				20	

**Dynamic Electrical Characteristics**

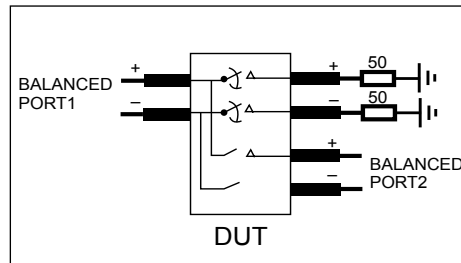
Parameter	Description	Test Conditions	Min.	Typ. <sup>(1)</sup>	Max.	Units
DDIL <sup>(2,3)</sup>	Differential Insertion Loss ( $V_{IN} = -10\text{dBm}$ , $DC = 0V$ )	$f = 100\text{MHz}$ $f = 1.25\text{GHz}$ $f = 2.5\text{GHz}$ $f = 4.0\text{GHz}$		-0.4 -0.6 -1.0 -1.7		dB
DDILOFF <sup>(2,3)</sup>	Differential Off Isolation	$f = 100\text{MHz}$ $f = 1.25\text{GHz}$ $f = 2.5\text{GHz}$ $f = 4.0\text{GHz}$		-59 -37 -27 -21		dB
DDRL <sup>(2)</sup>	Differential Return Loss	$f = 100\text{MHz}$ $f = 1.25\text{GHz}$ $f = 2.5\text{GHz}$ $f = 4.0\text{GHz}$		-27 -23.3 -23.3 -13.5		dB
DDNEXT <sup>(2,3)</sup>	Near End Crosstalk	$f = 100\text{MHz}$ $f = 1.25\text{GHz}$ $f = 2.5\text{GHz}$ $f = 4.0\text{GHz}$		-57 -38 -33 -32		dB
BW	-3dB Bandwidth			8.1		GHz

**Notes:**

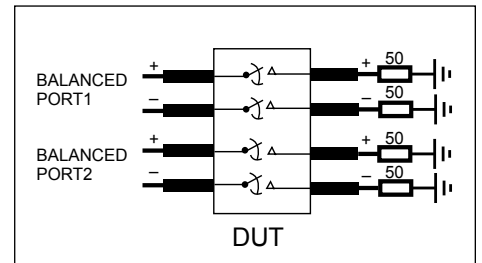
1. Guaranteed by design. Typical values are at  $V_{DD} = 3.3V$ ,  $T_A = 25^\circ\text{C}$  ambient and maximum loading.
2. S parameters are measured with our evaluation board made with Rogers (R04350) material. Trace width is 30 mil, length 540 mil, trace impedance is  $50\ \Omega$  (+/- 5%) and total insertion loss of the trace is 0.5dB@4GHz.
3. Measurement done with fixture embedding.



**Differential Insertion Loss and Return Test Circuit**

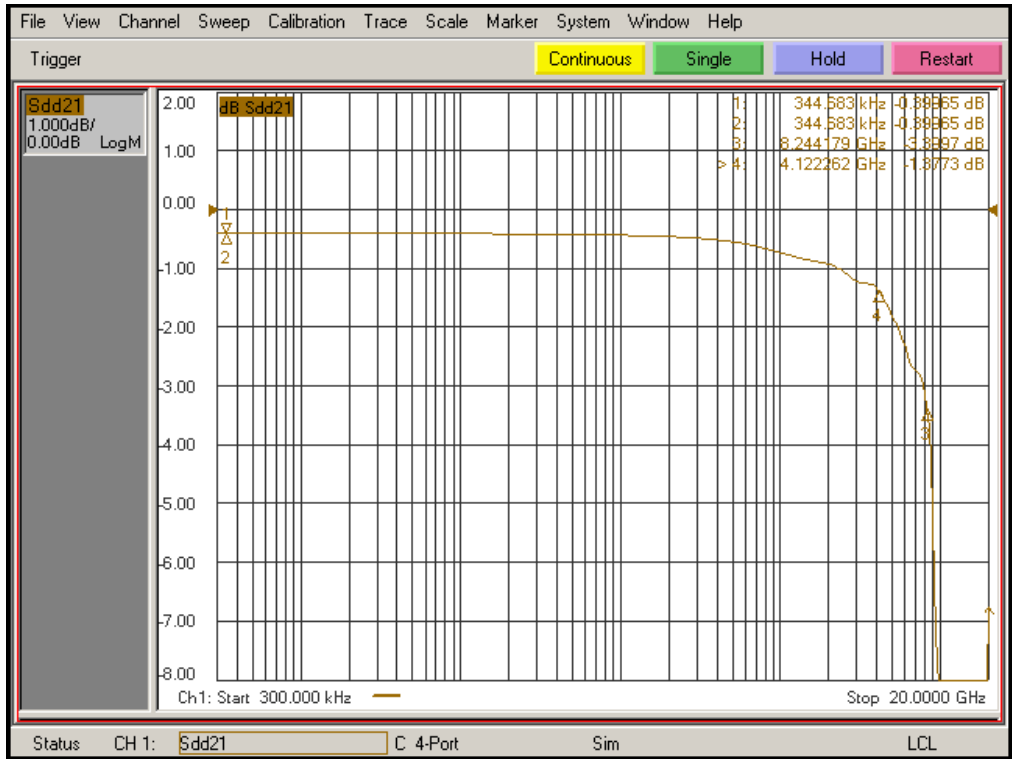


**Differential Off Isolation Test Circuit**

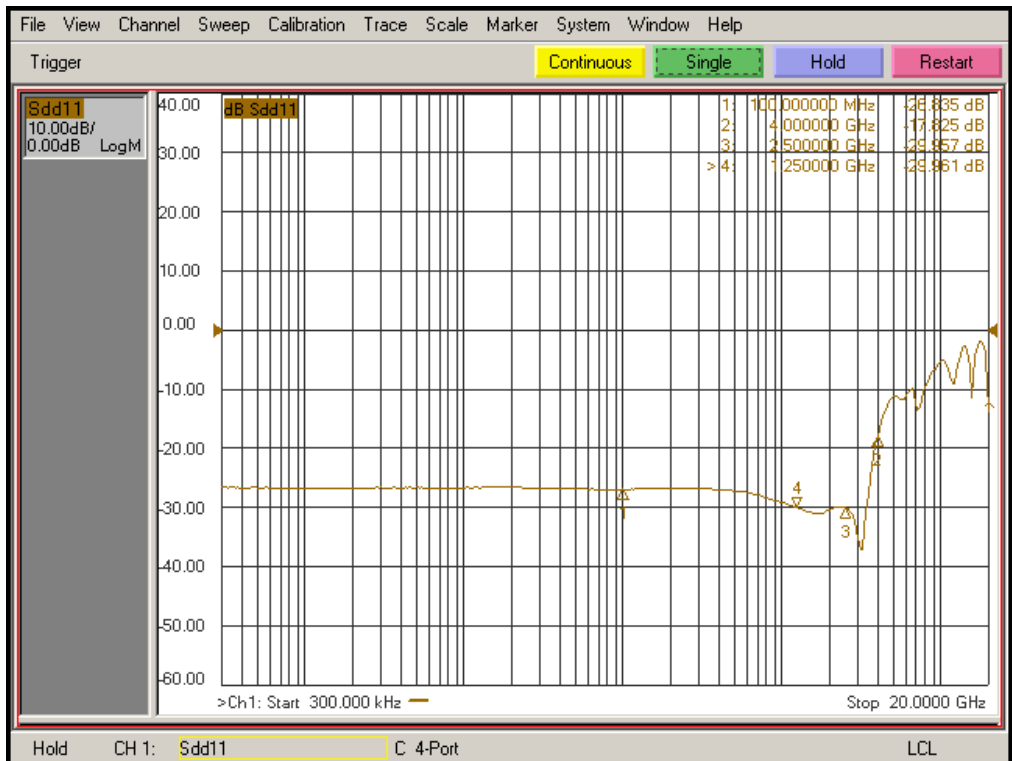


**Differential Near End Xtalk Test Circuit**

**PI3USB302-A**

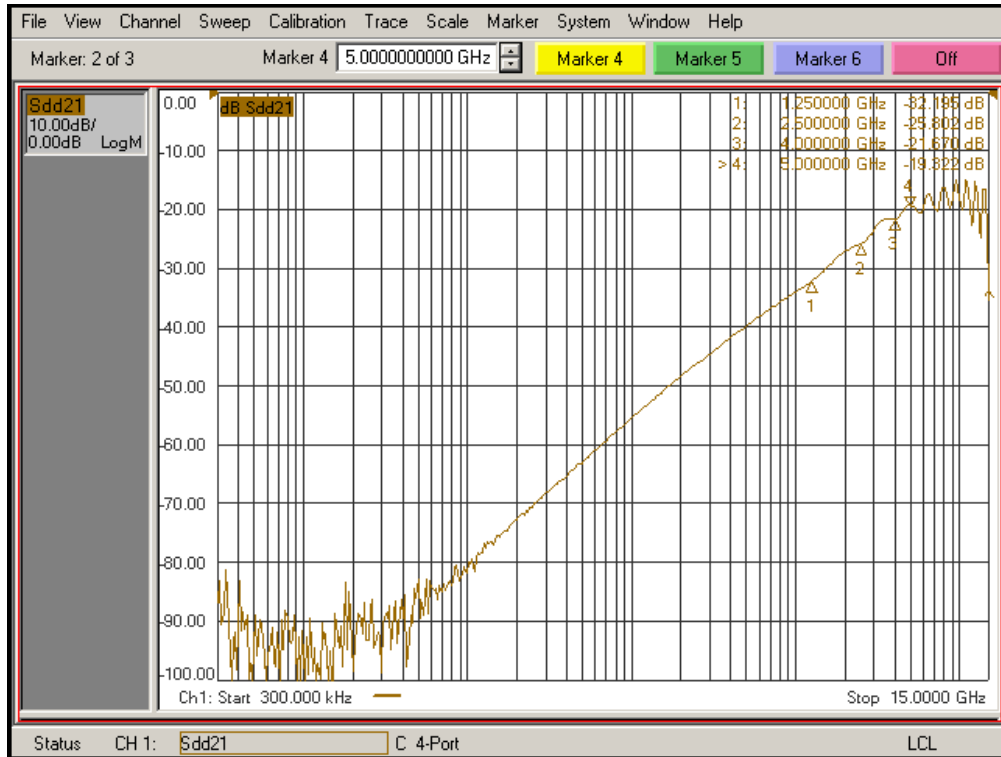


**Differential Insertion Loss**

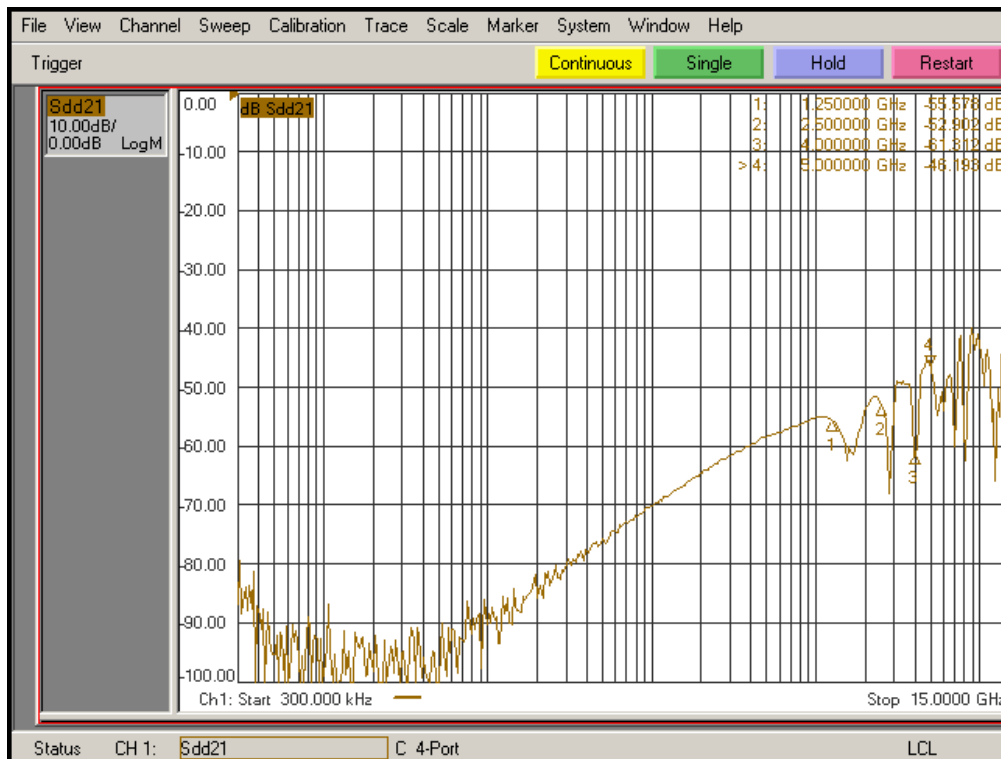


**Differential Return Loss**

**PI3USB302-A**

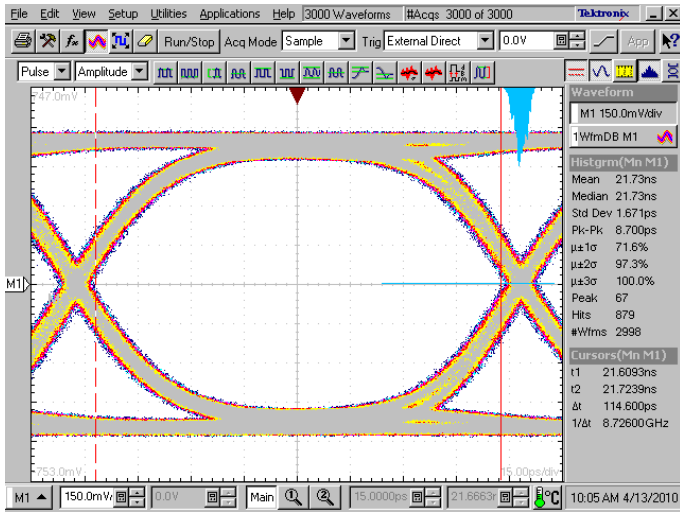


**Differential Off Isolation**

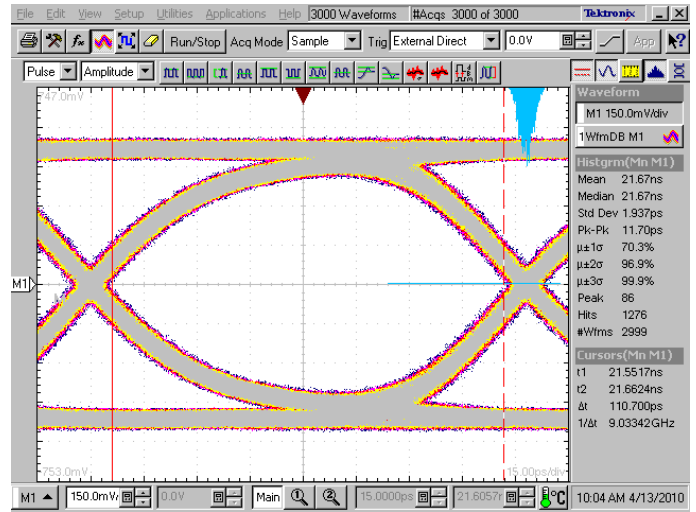


**Differential Crosstalk**

**PI3USB302-A**

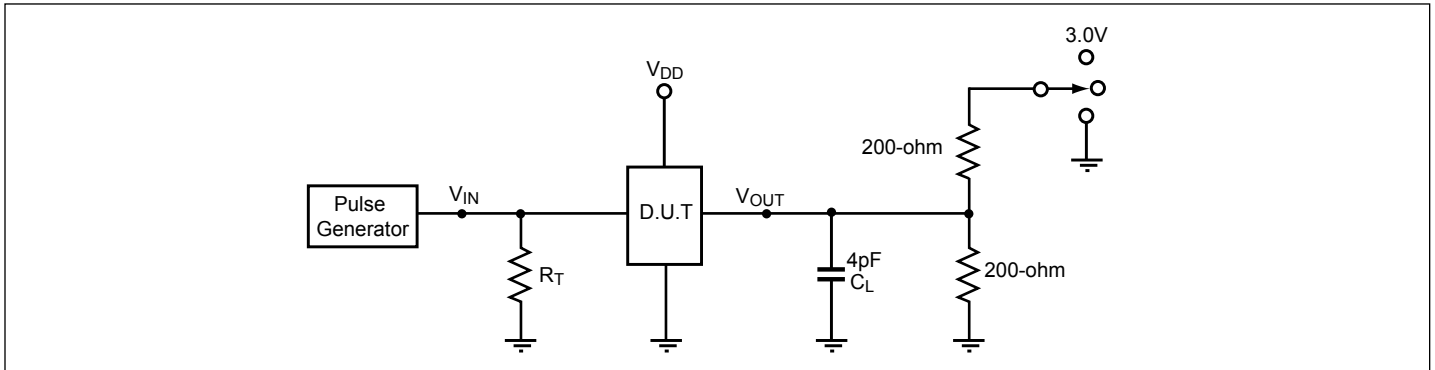


**5.0 Gbps RX signal eye without PI3USB302-A**



**5.0 Gbps RX signal eye with PI3USB302-A**

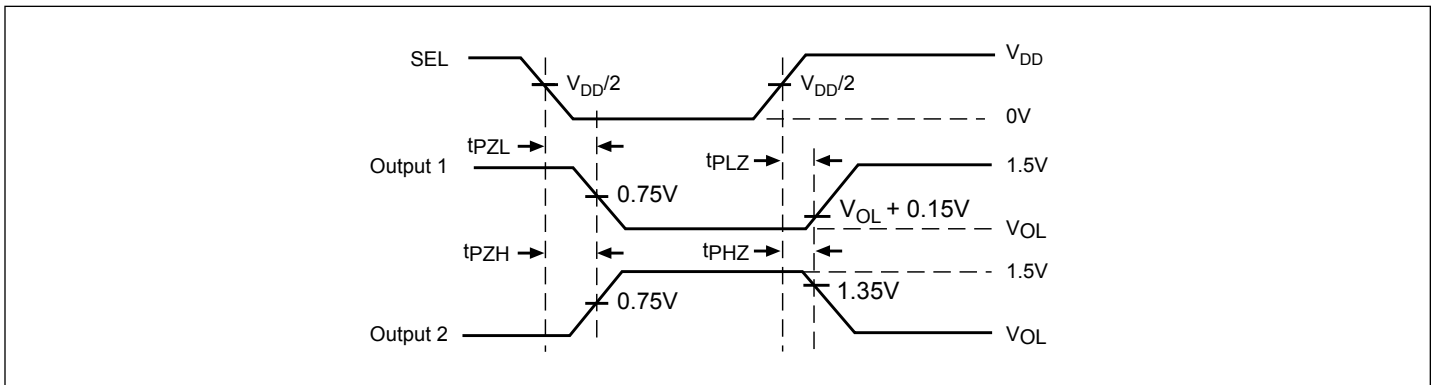
**Test Circuit for Electrical Characteristics**



**Switch Positions**

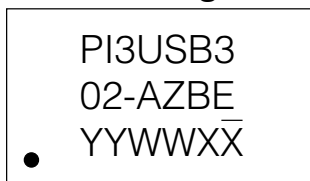
Test	Switch
t <sub>PLZ</sub> , t <sub>PZL</sub>	3.0V
t <sub>PHZ</sub> , t <sub>PZH</sub>	GND
Prop Delay	Open

**Switching Waveforms**



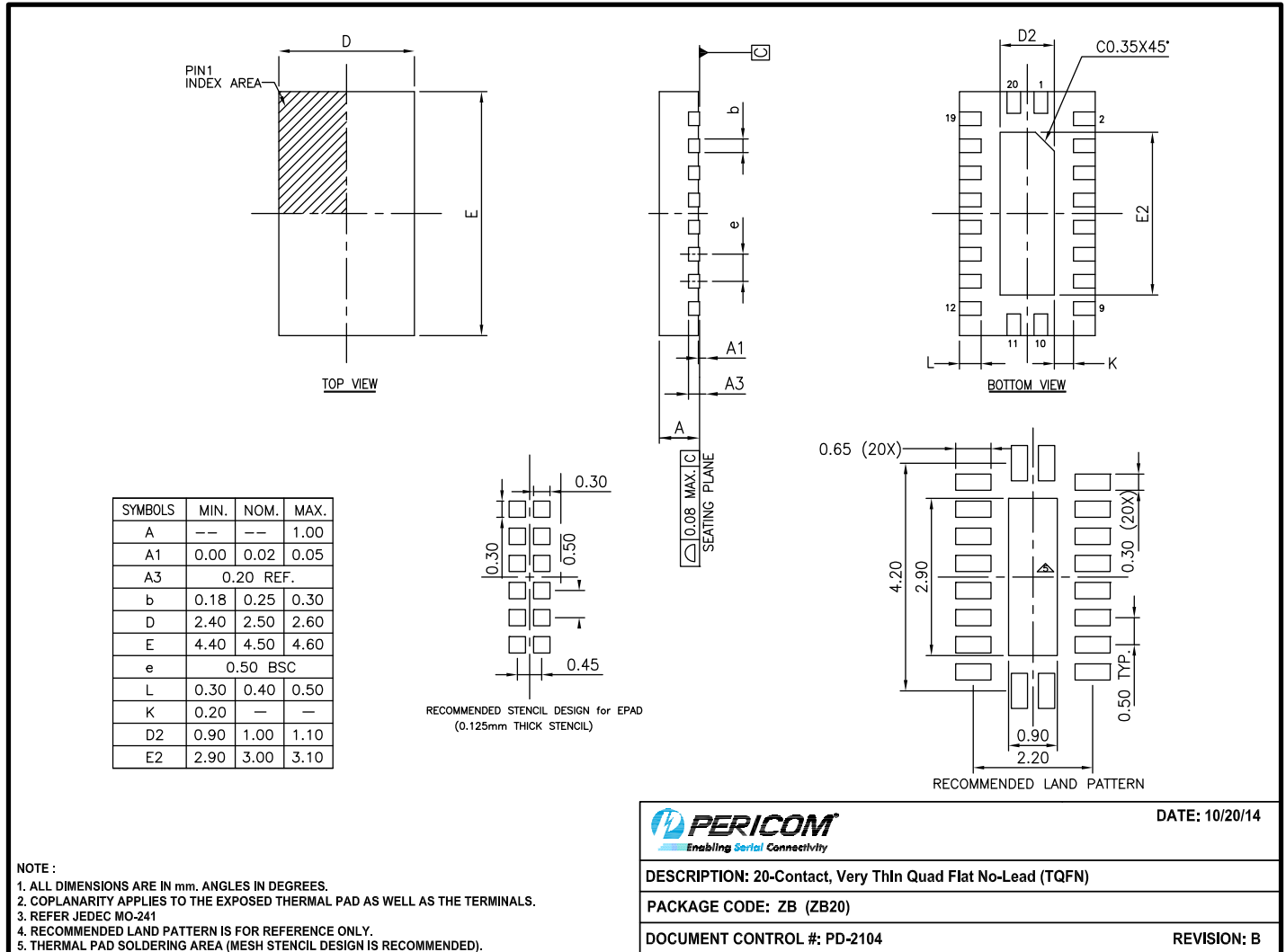
**Voltage Waveforms Enable and Disable Times**

**Part Marking**



YY: Year  
WW: Workweek  
1st X: Assembly Code  
2nd X: Fab Code

**Packaging Mechanical: 20-TQFN (ZB)**



**PERICOM**  
Enabling Serial Connectivity

DATE: 10/20/14

DESCRIPTION: 20-Contact, Very Thin Quad Flat No-Lead (TQFN)

PACKAGE CODE: ZB (ZB20)

DOCUMENT CONTROL #: PD-2104

REVISION: B

For latest package info.

please check: <http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/>

**Ordering Information**

Ordering Code	Package Code	Package Description
PI3USB302-AZBEX	ZB	20-Contact, Very Thin Quad Flat No-Lead (TQFN)

**Notes:**

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. E = Pb-free and Green
5. X suffix = Tape/Reel

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