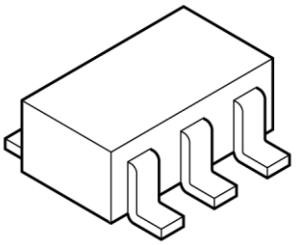
TOSHIBA CMOS Linear Integrated Circuit Silicon Monolithic

TC75S54F, TC75S54FU

Single Operational Amplifier

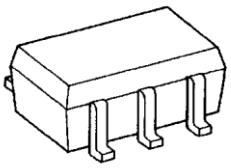
The TC75S54F/TC75S54FU is a CMOS single-operation amplifier which incorporates a phase compensation circuit. It is designed for use with a low-voltage, low-current power supply; this differentiates this device from conventional general-purpose bipolar op-amps.

TC75S54F



SSOP5-P-0.95 (SMV)

TC75S54FU



SSOP5-P-0.65A (USV)

Weight
SSOP5-P-0.95 : 0.014 g (typ.)
SSOP5-P-0.65A : 0.006 g (typ.)

Features

- Low-voltage operation : V_{DD} = ±0.9 to ±3.5 V or 1.8 to 7 V
- Low-current power supply : I_{DD} (V_{DD} = 3 V) = 100 μA (typ.)
- Built-in phase-compensated op-amp, obviating the need for any external device
- Ultra-compact package

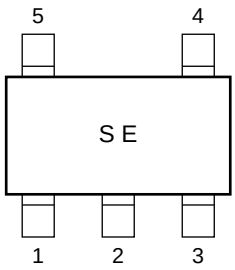
Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V_{DD}, V_{SS}	7	V
Differential input voltage	DV_{IN}	±7	V
Input voltage	V_{IN}	V_{DD} to V_{SS}	V
Power dissipation	P_D	200	mW
Operating temperature	T_{opr}	−40 to 85	°C
Storage temperature	T_{stg}	−55 to 125	°C

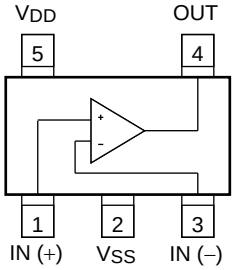
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production
1995-01

Marking (top view)



Pin Connection (top view)



Electrical Characteristics

DC Characteristics (V_{DD} = 3.0 V, V_{SS} = GND, Ta = 25°C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Input offset voltage	V_{IO}	1	R_S = 1 kΩ	—	2	10	mV
Input offset current	I_{IO}	—	—	—	1	—	pA
Input bias current	I_I	—	—	—	1	—	pA
Common mode input voltage	CMV_{IN}	2	—	0.0	—	2.1	V
Voltage gain (open loop)	G_V	—	—	60	70	—	dB
Maximum output voltage	V_{OH}	3	R_L ≥ 100 kΩ	2.9	—	—	V
	V_{OL}	4	R_L ≥ 100 kΩ	—	—	0.1	
Common mode input signal rejection ratio	CMRR	2	V_{IN} = 0.0 to 2.1 V	60	70	—	dB
Supply voltage rejection ratio	SVRR	1	V_{DD} = 1.8 to 7.0 V	60	70	—	dB
Supply current	I_{DD}	5	—	—	100	200	μA
Source current	I_{source}	6	—	100	200	—	μA
Sink current	I_{sink}	7	—	200	700	—	μA

DC Characteristics (V_{DD} = 1.8 V, V_{SS} = GND, Ta = 25°C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Input offset voltage	V_{IO}	1	R_S = 10 kΩ	—	2	10	mV
Input offset current	I_{IO}	—	—	—	1	—	pA
Input bias current	I_I	—	—	—	1	—	pA
Common mode input voltage	CMV_{IN}	2	—	0.2	—	0.9	V
Voltage gain (open loop)	G_V	—	—	60	70	—	dB
Maximum output voltage	V_{OH}	3	R_L ≥ 100 kΩ	1.7	—	—	V
	V_{OL}	4	R_L ≥ 100 kΩ	—	—	0.1	
Supply current	I_{DD}	5	—	—	80	160	μA
Source current	I_{source}	6	—	80	160	—	μA
Sink current	I_{sink}	7	—	200	600	—	μA

AC Characteristics (V_{DD} = 3.0 V, V_{SS} = GND, Ta = 25°C)

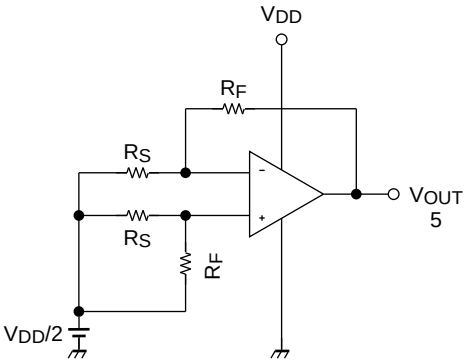
Characteristics	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Slew rate	SR	—	—	—	0.7	—	V/μs
Unity gain cross frequency	f_T	—	—	—	0.9	—	MHz

AC Characteristics (V_{DD} = 1.8 V, V_{SS} = GND, Ta = 25°C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Slew rate	SR	—	—	—	0.6	—	V/μs
Unity gain cross frequency	f_T	—	—	—	0.8	—	MHz

Test Circuit

1. SVRR, V_{IO}



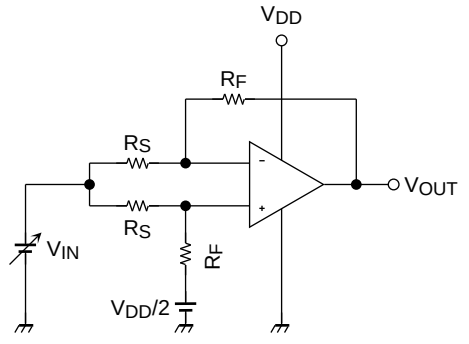
- SVRR
  For each of the two V_{DD} values, measure the V_{OUT} value, as indicated below, and calculate the value of SVRR using the equation shown.
  When V_{DD} = 1.8 V, V_{DD} = $V_{DD}1$ and V_{OUT} = $V_{OUT}1$
  When V_{DD} = 7.0 V, V_{DD} = $V_{DD}2$ and V_{OUT} = $V_{OUT}2$

$$SVRR = 20\log\left(\left|\frac{V_{OUT}1 - V_{OUT}2}{V_{DD}1 - V_{DD}2}\right| \times \frac{R_S}{R_F + R_S}\right)$$

- V_{IO}
  Measure the value of V_{OUT} and calculate the value of V_{IO} using the following equation.

$$V_{IO} = \left(V_{OUT} - \frac{V_{DD}}{2}\right) \times \frac{R_S}{R_F + R_S}$$

2. CMRR, CMV_{IN}

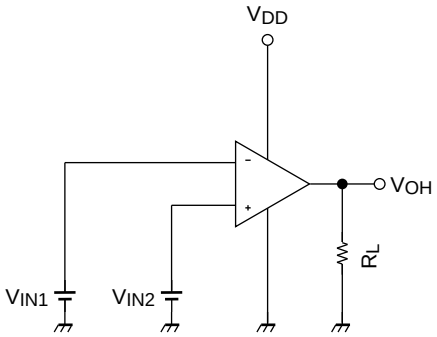


- CMRR
  Measure the V_{OUT} value, as indicated below, and calculate the value of the CMRR using the equation shown.
  When V_{IN} = 0.0 V, V_{IN} = $V_{IN}1$ and V_{OUT} = $V_{OUT}1$
  When V_{IN} = 2.1 V, V_{IN} = $V_{IN}2$ and V_{OUT} = $V_{OUT}2$

$$CMRR = 20\log\left(\left|\frac{V_{OUT}1 - V_{OUT}2}{V_{IN}1 - V_{IN}2}\right| \times \frac{R_S}{R_F + R_S}\right)$$

- CMV_{IN}
  Input range within which the CMRR specification guarantees V_{OUT} value (as varied by the V_{IN} value).

3. V_{OH}

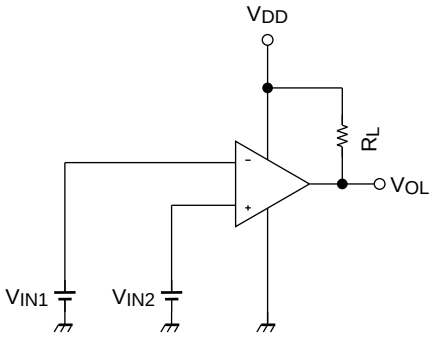


- V_{OH}

$$V_{IN1} = \frac{V_{DD}}{2} - 0.05\ V$$

$$V_{IN2} = \frac{V_{DD}}{2} + 0.05\ V$$

4. V_{OL}

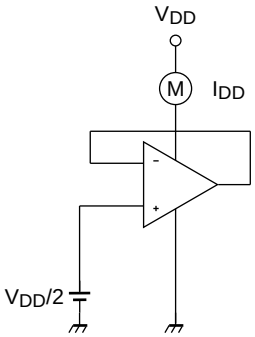


- V_{OL}

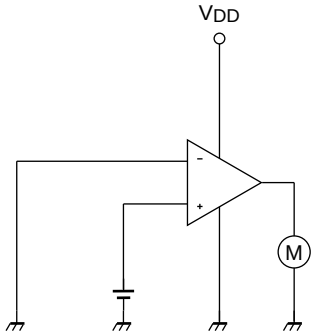
$$V_{IN1} = \frac{V_{DD}}{2} + 0.05\ V$$

$$V_{IN2} = \frac{V_{DD}}{2} - 0.05\ V$$

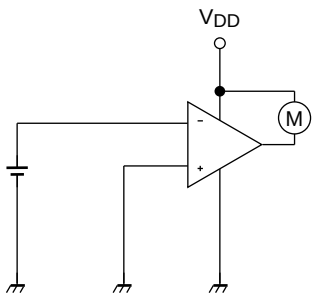
5. I_{DD}



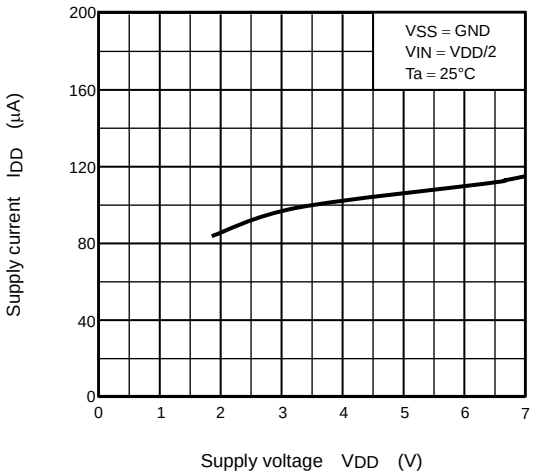
6. Isource



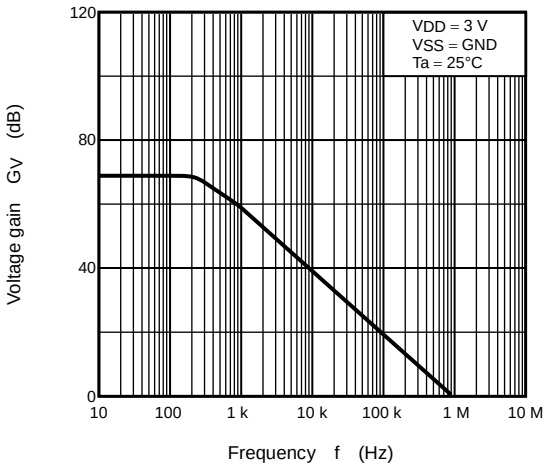
7. Isink



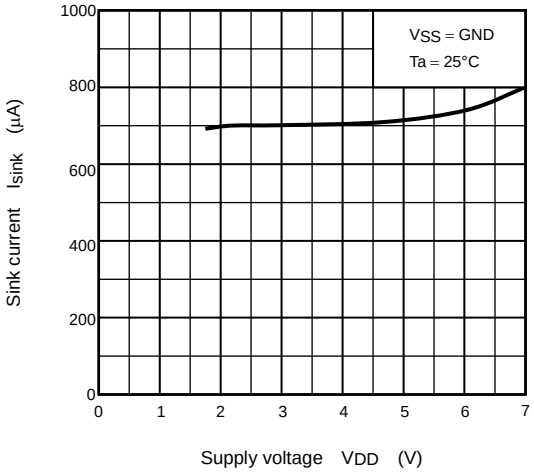
$I_{DD} - V_{DD}$



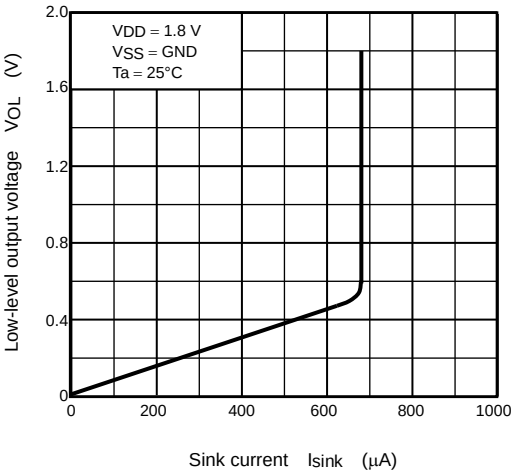
$G_V - f$



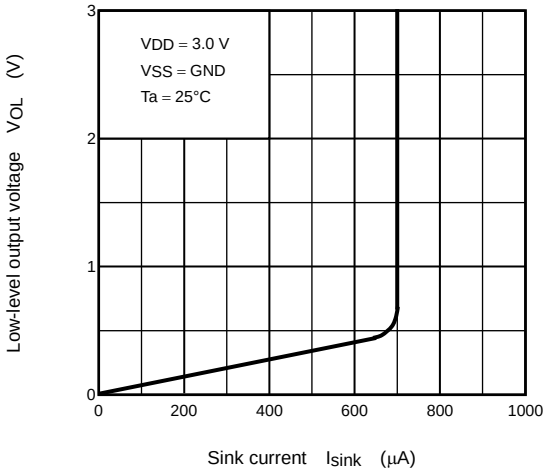
$I_{sink} - V_{DD}$



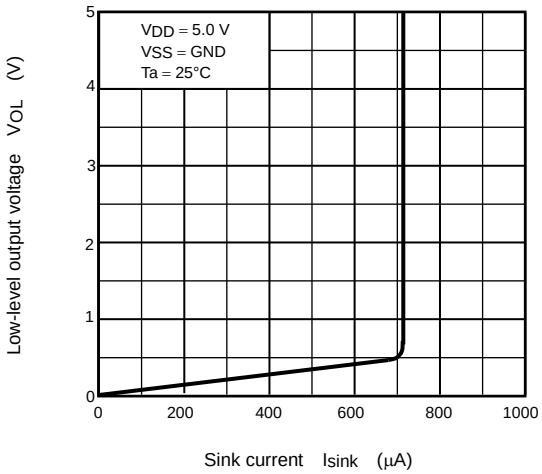
$V_{OL} - I_{sink}$



$V_{OL} - I_{sink}$

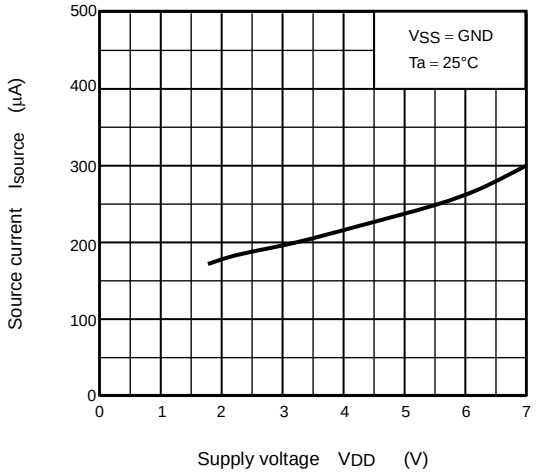


$V_{OL} - I_{sink}$

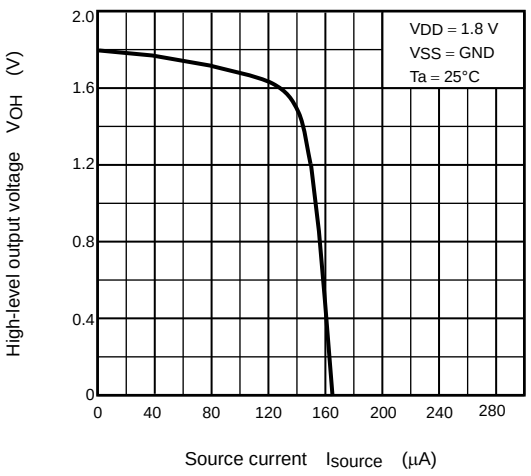


The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

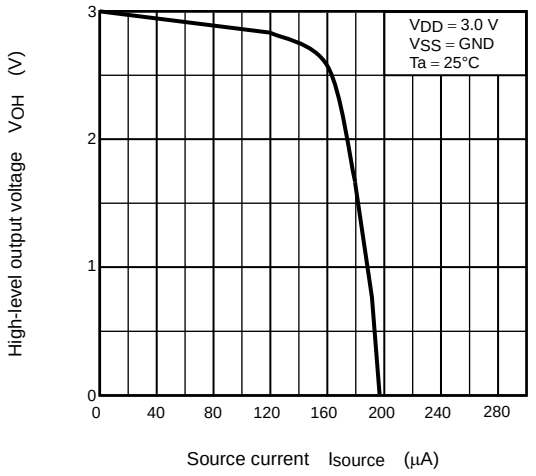
$I_{source} - V_{DD}$



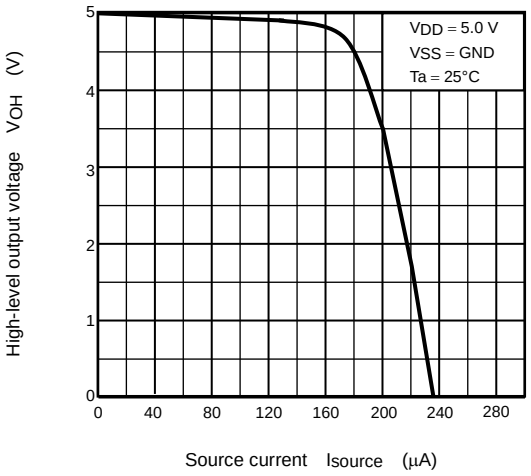
$V_{OH} - I_{source}$



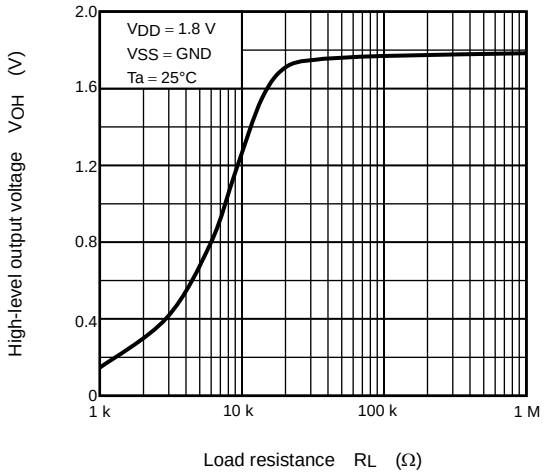
$V_{OH} - I_{source}$



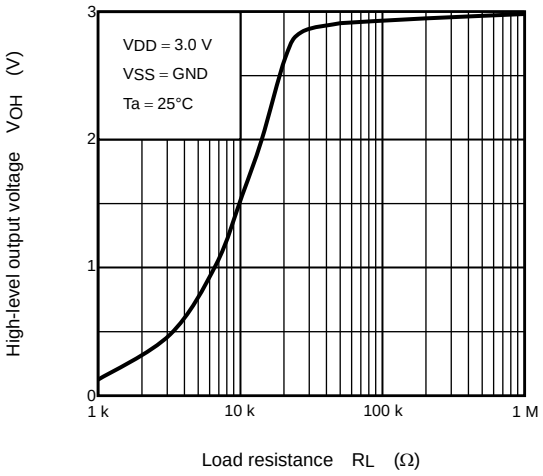
$V_{OH} - I_{source}$



$V_{OH} - R_L$

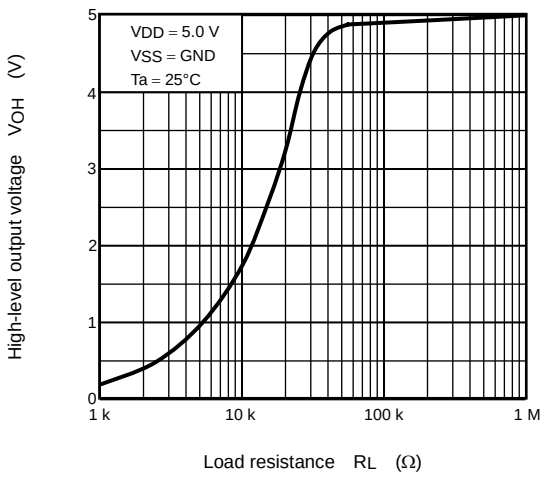


$V_{OH} - R_L$

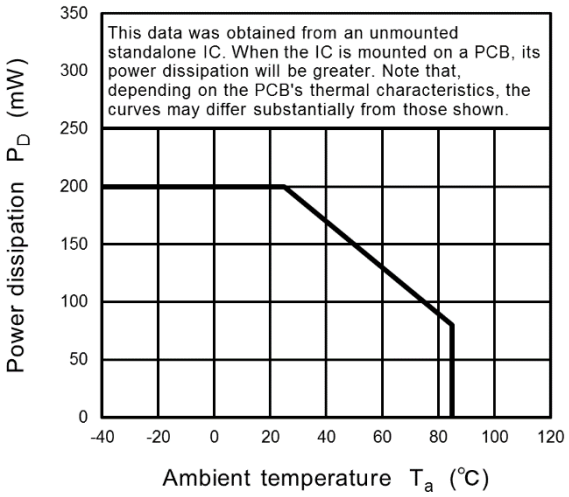


The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

V_{OH} – R_L



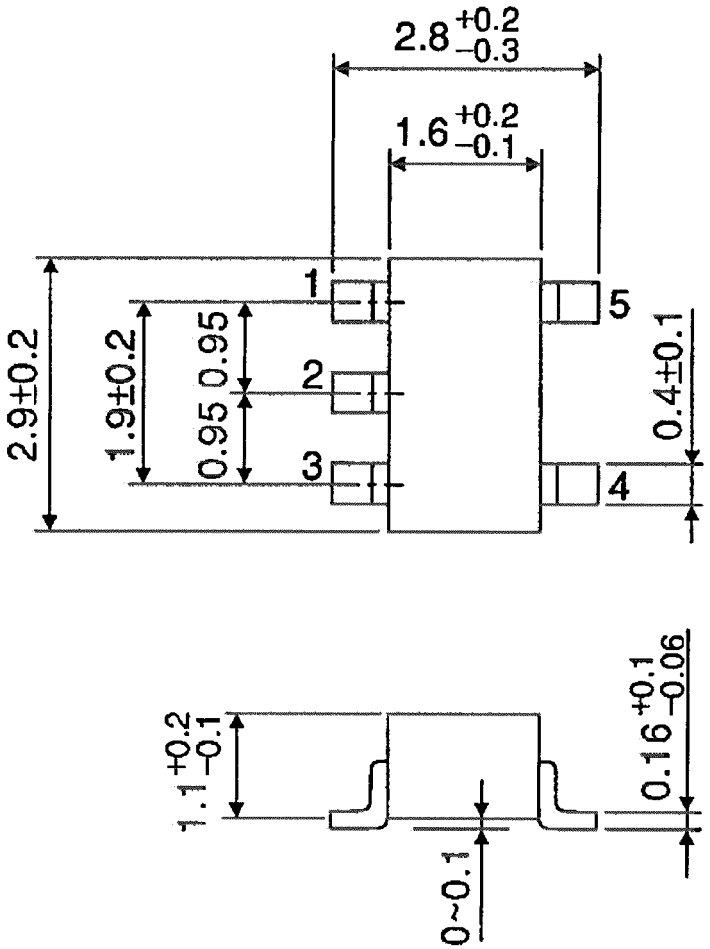
P_D – Ta



The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Package Dimensions

SSOP5-P-0.95 Unit : mm

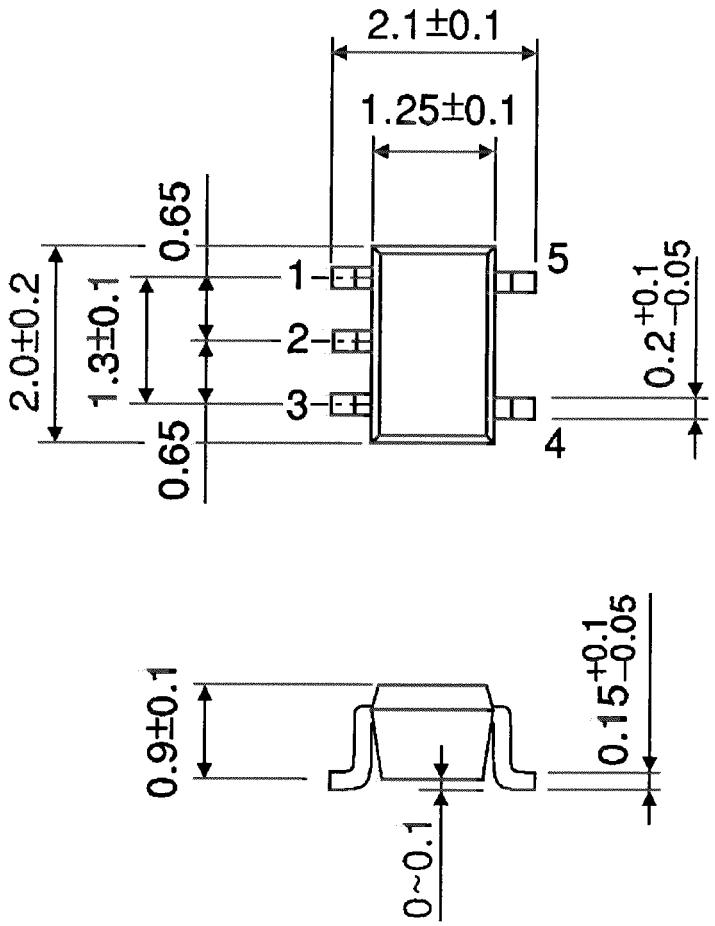


Weight: 0.014 g (typ.)

Package Dimensions

SSOP5-P-0.65A

Unit : mm



Weight: 0.006 g (typ.)

RESTRICTIONS ON PRODUCT USE

Toshiba Corporation and its subsidiaries and affiliates are collectively referred to as “TOSHIBA”.
Hardware, software and systems described in this document are collectively referred to as “Product”.

- TOSHIBA reserves the right to make changes to the information in this document and related Product without notice.
- This document and any information herein may not be reproduced without prior written permission from TOSHIBA. Even with TOSHIBA's written permission, reproduction is permissible only if reproduction is without alteration/omission.
- Though TOSHIBA works continually to improve Product's quality and reliability, Product can malfunction or fail. Customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of Product could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Before customers use the Product, create designs including the Product, or incorporate the Product into their own applications, customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and (b) the instructions for the application with which the Product will be used with or for. Customers are solely responsible for all aspects of their own product design or applications, including but not limited to (a) determining the appropriateness of the use of this Product in such design or applications; (b) evaluating and determining the applicability of any information contained in this document, or in charts, diagrams, programs, algorithms, sample application circuits, or any other referenced documents; and (c) validating all operating parameters for such designs and applications. **TOSHIBA ASSUMES NO LIABILITY FOR CUSTOMERS' PRODUCT DESIGN OR APPLICATIONS.**
- **PRODUCT IS NEITHER INTENDED NOR WARRANTED FOR USE IN EQUIPMENTS OR SYSTEMS THAT REQUIRE EXTRAORDINARILY HIGH LEVELS OF QUALITY AND/OR RELIABILITY, AND/OR A MALFUNCTION OR FAILURE OF WHICH MAY CAUSE LOSS OF HUMAN LIFE, BODILY INJURY, SERIOUS PROPERTY DAMAGE AND/OR SERIOUS PUBLIC IMPACT ("UNINTENDED USE").** Except for specific applications as expressly stated in this document, Unintended Use includes, without limitation, equipment used in nuclear facilities, equipment used in the aerospace industry, lifesaving and/or life supporting medical equipment, equipment used for automobiles, trains, ships and other transportation, traffic signaling equipment, equipment used to control combustions or explosions, safety devices, elevators and escalators, and devices related to power plant. **IF YOU USE PRODUCT FOR UNINTENDED USE, TOSHIBA ASSUMES NO LIABILITY FOR PRODUCT.** For details, please contact your TOSHIBA sales representative or contact us via our website.
- Do not disassemble, analyze, reverse-engineer, alter, modify, translate or copy Product, whether in whole or in part.
- Product shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable laws or regulations.
- The information contained herein is presented only as guidance for Product use. No responsibility is assumed by TOSHIBA for any infringement of patents or any other intellectual property rights of third parties that may result from the use of Product. No license to any intellectual property right is granted by this document, whether express or implied, by estoppel or otherwise.
- **ABSENT A WRITTEN SIGNED AGREEMENT, EXCEPT AS PROVIDED IN THE RELEVANT TERMS AND CONDITIONS OF SALE FOR PRODUCT, AND TO THE MAXIMUM EXTENT ALLOWABLE BY LAW, TOSHIBA (1) ASSUMES NO LIABILITY WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (2) DISCLAIMS ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO SALE, USE OF PRODUCT, OR INFORMATION, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.**
- Do not use or otherwise make available Product or related software or technology for any military purposes, including without limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile technology products (mass destruction weapons). Product and related software and technology may be controlled under the applicable export laws and regulations including, without limitation, the Japanese Foreign Exchange and Foreign Trade Law and the U.S. Export Administration Regulations. Export and re-export of Product or related software or technology are strictly prohibited except in compliance with all applicable export laws and regulations.
- Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. Please use Product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. **TOSHIBA ASSUMES NO LIABILITY FOR DAMAGES OR LOSSES OCCURRING AS A RESULT OF NONCOMPLIANCE WITH APPLICABLE LAWS AND REGULATIONS.**

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Toshiba:
TC75S54F,LF