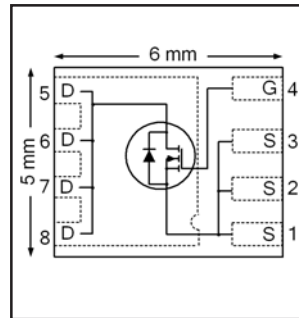


HEXFET® Power MOSFET

V_{DS}	30	V
$V_{GS \text{ max}}$	± 20	V
$R_{DS(on) \text{ max}}$ (@ $V_{GS} = 10V$)	4.1	mΩ
(@ $V_{GS} = 4.5V$)	6.3	
$Q_g \text{ typ.}$	14	nC
I_D (@ $T_{c(Bottom)} = 25^\circ C$)	50 ⑦	A



Applications

- Synchronous MOSFET for high frequency buck converters

Features and Benefits

Features

Low Thermal Resistance to PCB (< 2.3°C/W)
Low Profile (<1.2mm)
Industry-Standard Pinout
Compatible with Existing Surface Mount Techniques
RoHS Compliant Containing no Lead, no Bromide and no Halogen
MSL1, Consumer Qualification

results in
⇒

Benefits

Enable better thermal dissipation
Increased Power Density
Multi-Vendor Compatibility
Easier Manufacturing
Environmentally Friendlier
Increased Reliability

Orderable part number	Package Type	Standard Pack		Note
		Form	Quantity	
IRFH8324TRPBF	PQFN 5mm x 6mm	Tape and Reel	4000	
IRFH8324TR2PBF	PQFN 5mm x 6mm	Tape and Reel	400	EOL notice #259

Absolute Maximum Ratings

	Parameter	Max.	Units
V_{DS}	Drain-to-Source Voltage	30	V
V_{GS}	Gate-to-Source Voltage	± 20	
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	23	A
$I_D @ T_A = 70^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	18	
$I_D @ T_{c(Bottom)} = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	90 ⑥ ⑦	
$I_D @ T_{c(Bottom)} = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	57 ⑥ ⑦	
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$ (Package Limited)	50 ⑦	
I_{DM}	Pulsed Drain Current ①	200	
$P_D @ T_A = 25^\circ C$	Power Dissipation ⑤	3.6	W
$P_D @ T_{c(Bottom)} = 25^\circ C$	Power Dissipation ⑤	54	
	Linear Derating Factor ③	0.029	W/°C
T_J T_{STG}	Operating Junction and Storage Temperature Range	-55 to + 150	°C

Notes ① through ⑥ are on page 9

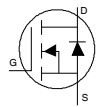
Static @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
BV_{DSS}	Drain-to-Source Breakdown Voltage	30	—	—	V	$V_{GS} = 0V, I_D = 250\mu A$
$\Delta BV_{DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	0.019	—	V/ $^\circ\text{C}$	Reference to 25°C , $I_D = 1.0\text{mA}$
$R_{DS(on)}$	Static Drain-to-Source On-Resistance	—	3.3	4.1	$\text{m}\Omega$	$V_{GS} = 10V, I_D = 20A$ ③
		—	5.0	6.3		$V_{GS} = 4.5V, I_D = 16A$ ③
$V_{GS(th)}$	Gate Threshold Voltage	1.35	1.8	2.35	V	$V_{DS} = V_{GS}, I_D = 50\mu A$
$\Delta V_{GS(th)}$	Gate Threshold Voltage Coefficient	—	-6.2	—	$\text{mV}/^\circ\text{C}$	
I_{DSS}	Drain-to-Source Leakage Current	—	—	1.0	μA	$V_{DS} = 24V, V_{GS} = 0V$
		—	—	150		$V_{DS} = 24V, V_{GS} = 0V, T_J = 125^\circ\text{C}$
I_{GSS}	Gate-to-Source Forward Leakage	—	—	100	nA	$V_{GS} = 20V$
	Gate-to-Source Reverse Leakage	—	—	-100		$V_{GS} = -20V$
g_{fs}	Forward Transconductance	72	—	—	S	$V_{DS} = 10V, I_D = 20A$
Q_g	Total Gate Charge	—	31	—	nC	$V_{GS} = 10V, V_{DS} = 15V, I_D = 20A$
Q_g	Total Gate Charge	—	14	—	nC	$V_{DS} = 15V$ $V_{GS} = 4.5V$ $I_D = 20A$
Q_{gs1}	Pre-V _{th} Gate-to-Source Charge	—	4.4	—		
Q_{gs2}	Post-V _{th} Gate-to-Source Charge	—	2.2	—		
Q_{gd}	Gate-to-Drain Charge	—	3.5	—		
Q_{godr}	Gate Charge Overdrive	—	3.9	—		
Q_{sw}	Switch Charge ($Q_{gs2} + Q_{gd}$)	—	5.7	—	nC	$V_{DS} = 16V, V_{GS} = 0V$
Q_{oss}	Output Charge	—	13	—		
R_G	Gate Resistance	—	1.1	—	Ω	
$t_{d(on)}$	Turn-On Delay Time	—	13	—	ns	$V_{DD} = 15V, V_{GS} = 4.5V$ $I_D = 20A$ $R_G = 1.8\Omega$
t_r	Rise Time	—	26	—		
$t_{d(off)}$	Turn-Off Delay Time	—	14	—		
t_f	Fall Time	—	8.5	—		
C_{iss}	Input Capacitance	—	2380	—	pF	$V_{GS} = 0V$ $V_{DS} = 10V$ $f = 1.0\text{MHz}$
C_{oss}	Output Capacitance	—	500	—		
C_{rss}	Reverse Transfer Capacitance	—	205	—		

Avalanche Characteristics

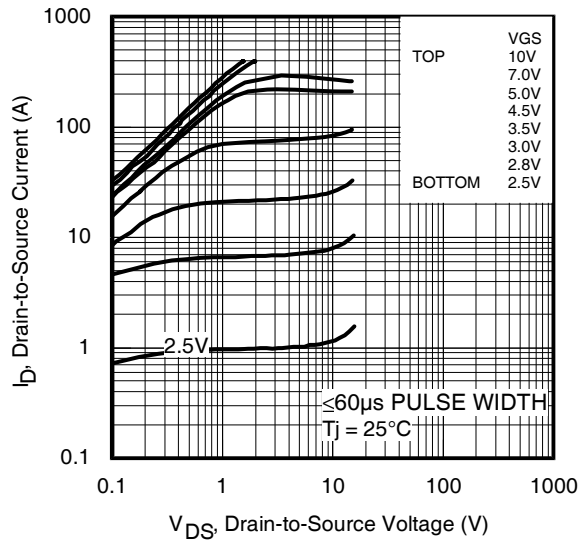
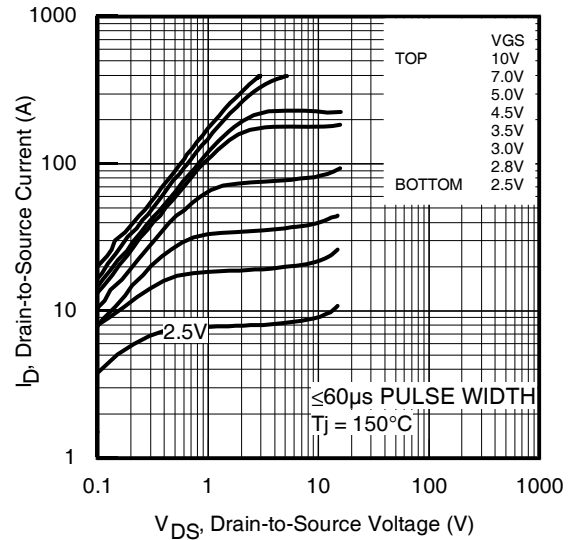
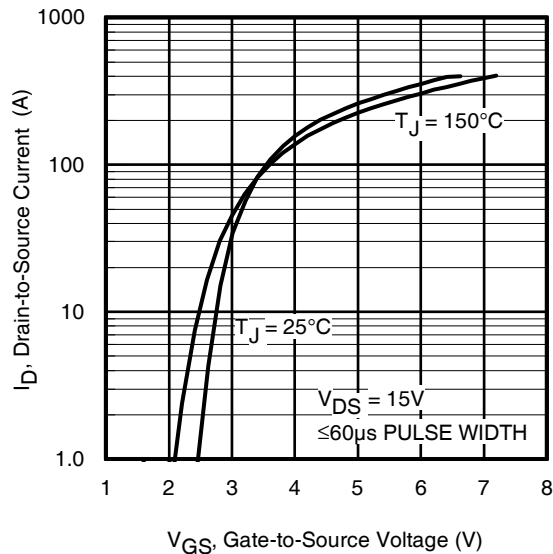
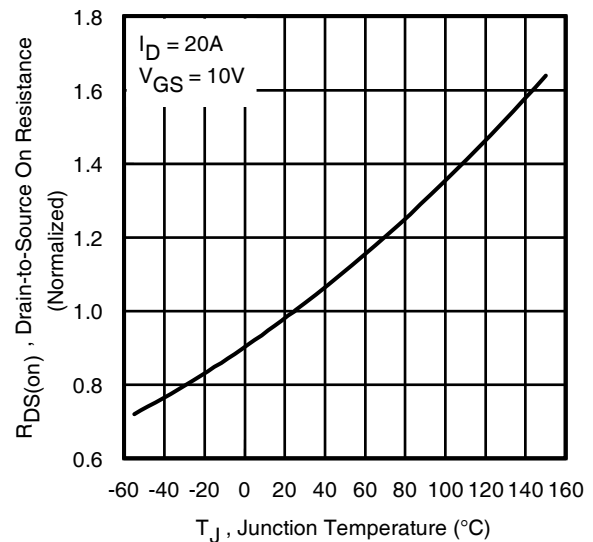
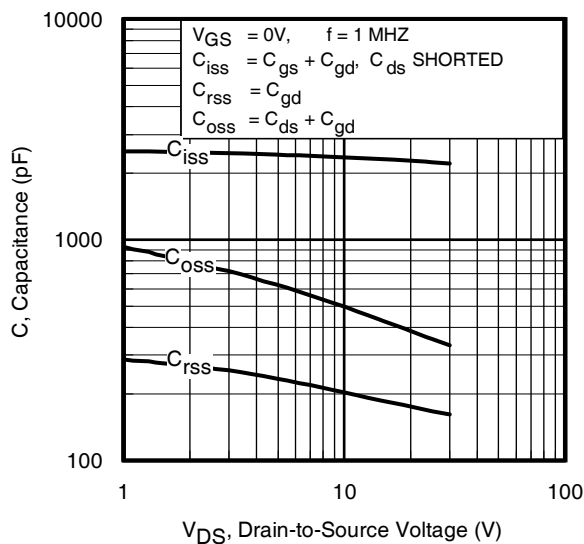
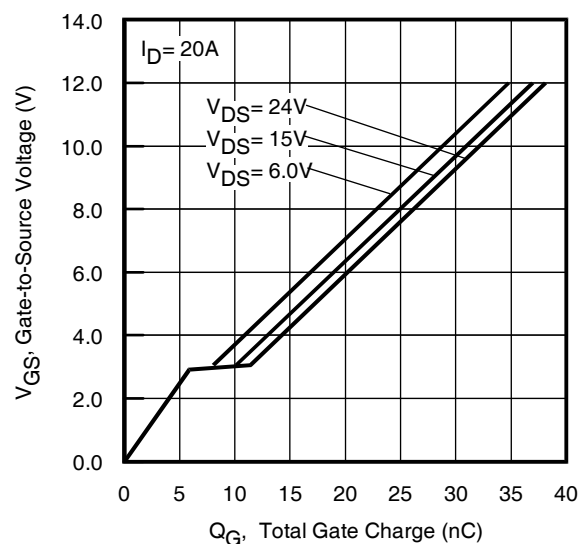
	Parameter	Typ.	Max.	Units
E_{AS}	Single Pulse Avalanche Energy ②	—	94	mJ
I_{AR}	Avalanche Current ①	—	20	A

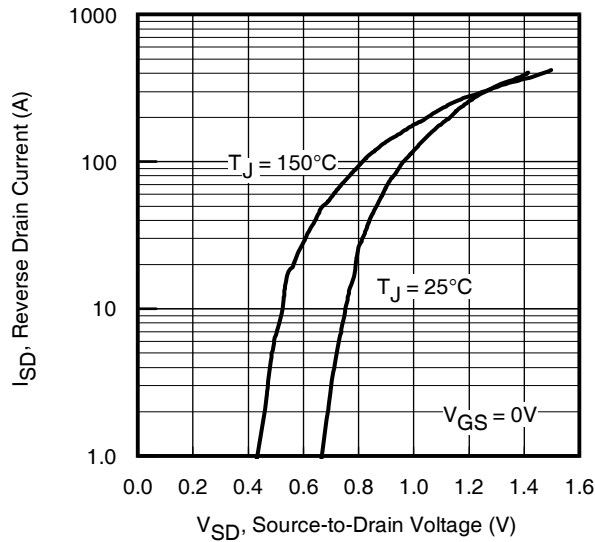
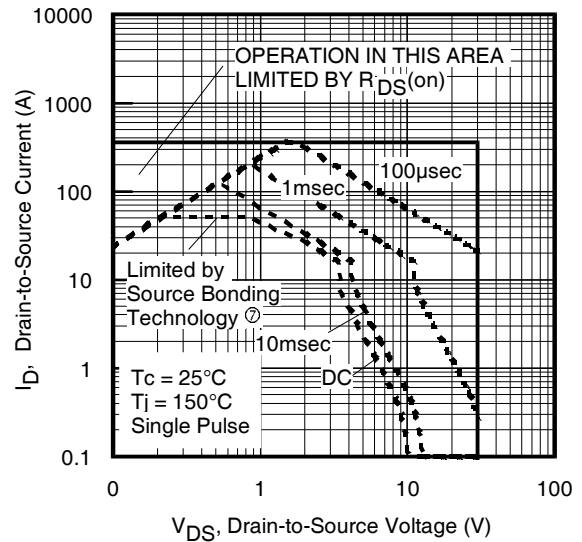
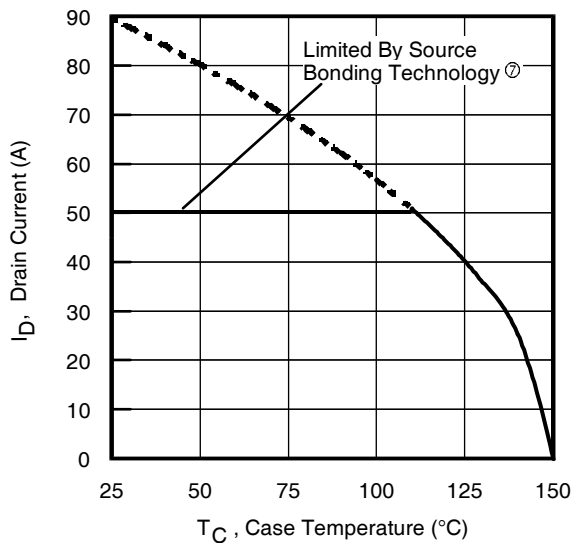
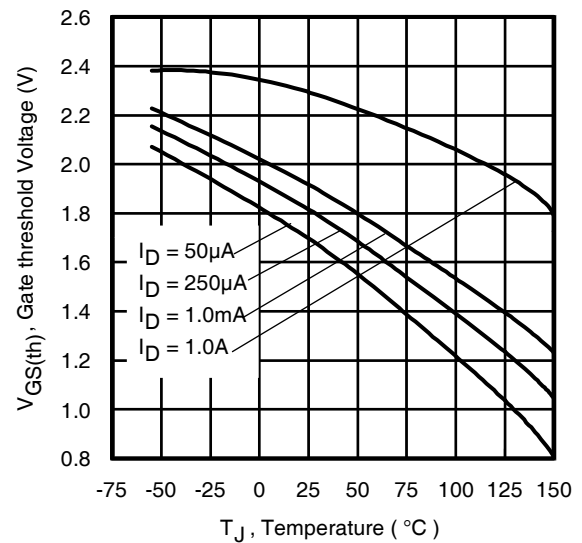
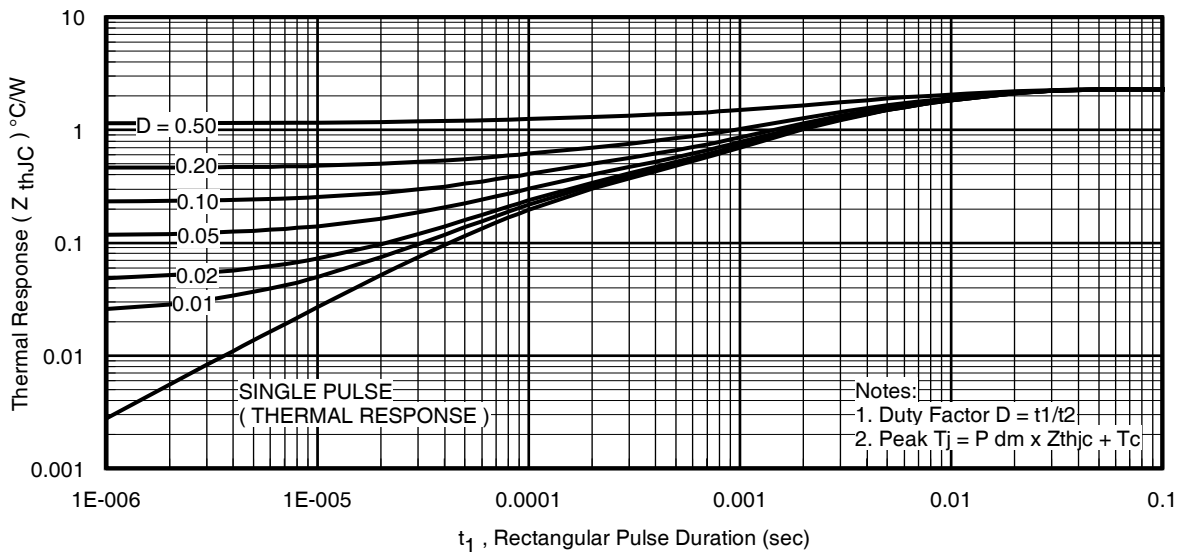
Diode Characteristics

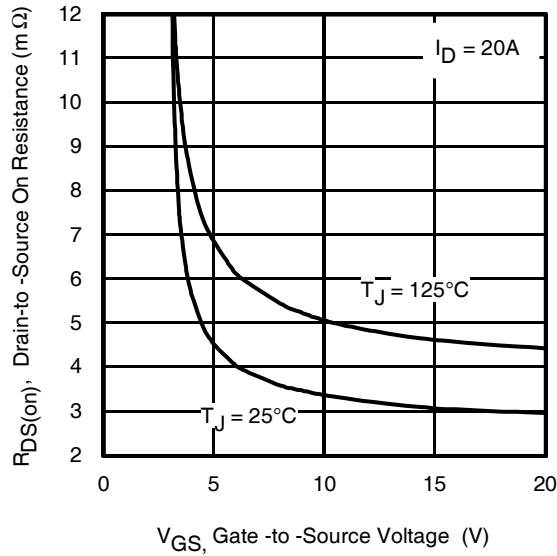
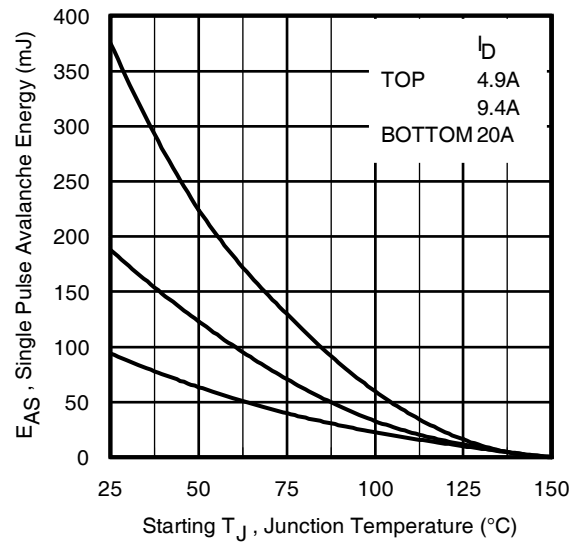
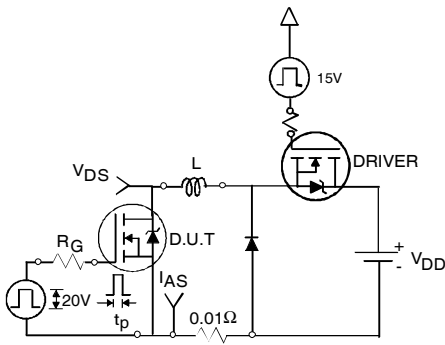
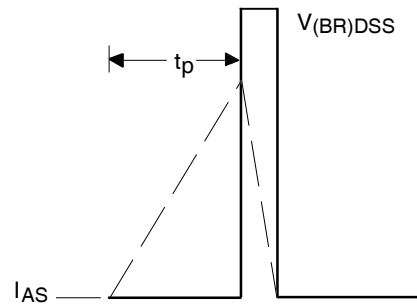
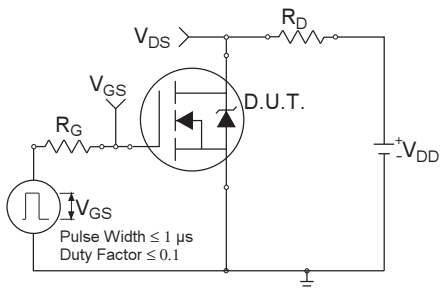
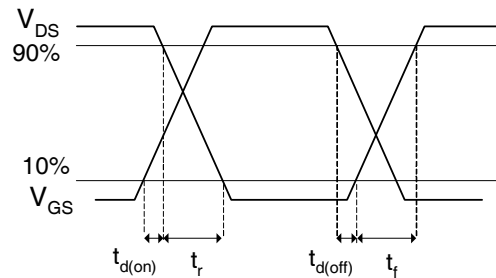
	Parameter	Min.	Typ.	Max.	Units	Conditions
I_S	Continuous Source Current (Body Diode)	—	—	50 ⑦	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I_{SM}	Pulsed Source Current (Body Diode) ①	—	—	200		
V_{SD}	Diode Forward Voltage	—	—	1.0	V	$T_J = 25^\circ\text{C}, I_S = 20A, V_{GS} = 0V$ ③
t_{rr}	Reverse Recovery Time	—	16	24	ns	$T_J = 25^\circ\text{C}, I_F = 20A, V_{DD} = 15V$
Q_{rr}	Reverse Recovery Charge	—	25	38	nC	$di/dt = 360 A/\mu s$ ③
t_{on}	Forward Turn-On Time	Time is dominated by parasitic Inductance				

Thermal Resistance

	Parameter	Typ.	Max.	Units
$R_{\theta JC}$ (Bottom)	Junction-to-Case ④	—	2.3	$^\circ\text{C}/W$
$R_{\theta JC}$ (Top)	Junction-to-Case ④	—	32	
$R_{\theta JA}$	Junction-to-Ambient ⑤	—	35	
$R_{\theta JA} (<10s)$	Junction-to-Ambient ⑤	—	23	


Fig 1. Typical Output Characteristics

Fig 2. Typical Output Characteristics

Fig 3. Typical Transfer Characteristics

Fig 4. Normalized On-Resistance vs. Temperature

Fig 5. Typical Capacitance vs. Drain-to-Source Voltage

Fig 6. Typical Gate Charge vs. Gate-to-Source Voltage


Fig 7. Typical Source-Drain Diode Forward Voltage

Fig 8. Maximum Safe Operating Area

Fig 9. Maximum Drain Current vs. Case (Bottom) Temperature

Fig 10. Threshold Voltage vs. Temperature

Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case (Bottom)


Fig 12. On-Resistance vs. Gate Voltage

Fig 13. Maximum Avalanche Energy vs. Drain Current

Fig 14a. Unclamped Inductive Test Circuit

Fig 14b. Unclamped Inductive Waveforms

Fig 15a. Switching Time Test Circuit

Fig 15b. Switching Time Waveforms

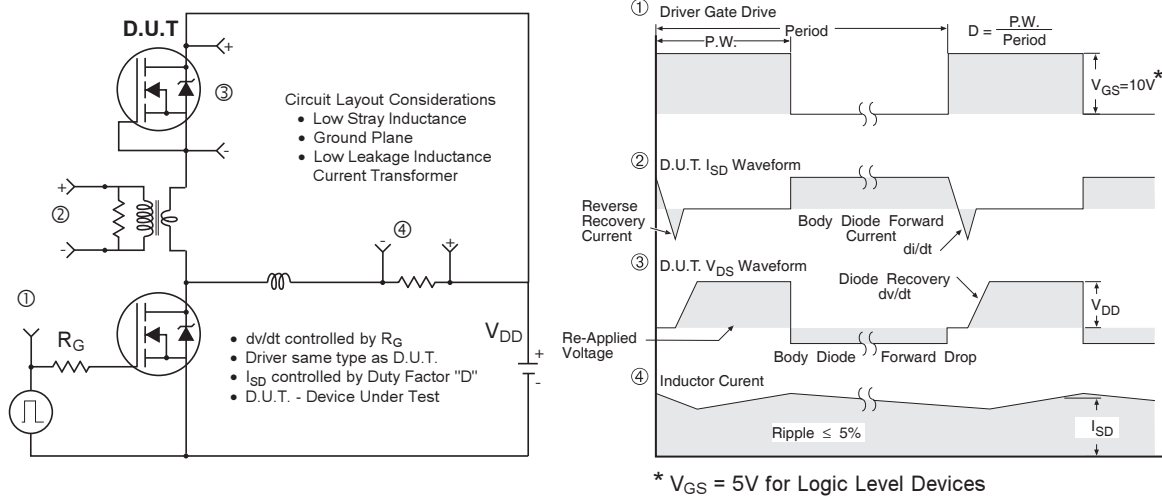


Fig 16. Peak Diode Recovery dv/dt Test Circuit for N-Channel HEXFET® Power MOSFETs

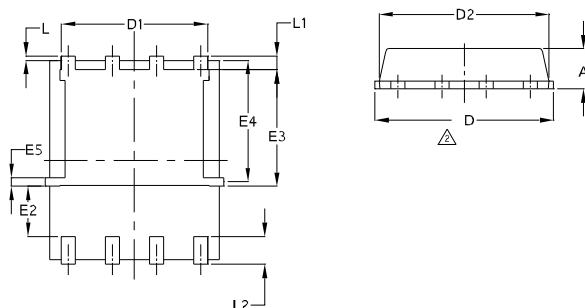


Fig 17. Gate Charge Test Circuit



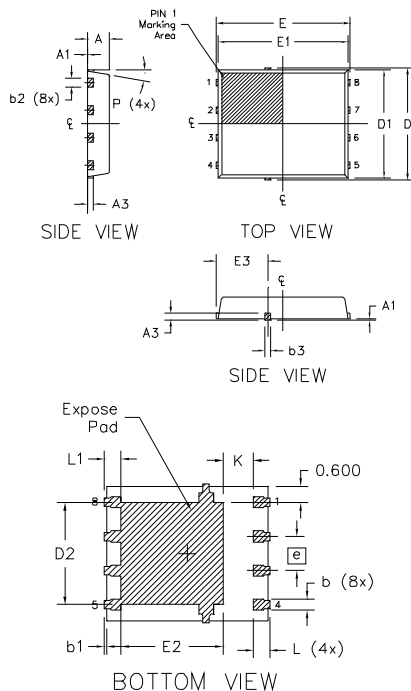
Fig 18. Gate Charge Waveform

PQFN 5x6 Outline "G" Package Details



SYMBOL	COMMON			
	MM		INCH	
	MIN.	MAX.	MIN.	MAX.
A	0.90	1.17	0.0354	0.0461
b	0.33	0.48	0.0130	0.0189
C	0.195	0.300	0.0077	0.0118
D	4.80	5.15	0.1890	0.2028
D1	3.91	4.31	0.1539	0.1697
D2	4.80	5.00	0.1890	0.1968
E	5.90	6.15	0.2323	0.2421
E1	5.65	6.00	0.2224	0.2362
E2	1.51	—	0.0594	—
E3	3.32	3.78	0.1307	0.1480
E4	3.42	3.58	0.1346	0.1409
E5	0.18	0.32	0.0071	0.0126
e	1.27	BSC	0.050	BSC
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.66	0.0150	0.0260
L2	0.51	0.86	0.0201	0.0339
I	0	0.18	0	0.0071

PQFN 5x6 Outline "G" Package Details



DIM SYMBOL	MILLIMETERS		INCH	
	MIN.	MAX.	MIN.	MAX.
A	0.950	1.050	0.0374	0.0413
A1	0.000	0.050	0.0000	0.0020
A3	0.254	REF	0.0100	REF
b	0.310	0.510	0.0122	0.0201
b1	0.025	0.125	0.0010	0.0049
b2	0.210	0.410	0.0083	0.0161
b3	0.180	0.450	0.0071	0.0177
D	5.150	BSC	0.2028	BSC
D1	5.000	BSC	0.1969	BSC
D2	3.700	3.900	0.1457	0.1535
E	6.150	BSC	0.2421	BSC
E1	6.000	BSC	0.2362	BSC
E2	3.560	3.760	0.1402	0.1488
E3	2.270	2.470	0.0894	0.0972
e	1.27	REF	0.050	REF
K	0.830	1.400	0.0327	0.0551
L	0.510	0.710	0.0201	0.0280
L1	0.510	0.710	0.0201	0.0280
P	10 deg	12 deg	0 deg	12 deg

1. Dimensions and toleranceing confirm to ASME Y14.5M-1994
2. Dimension L represents terminal full back from package edge up to 0.1mm is acceptable
3. Coplanarity applies to the expose Heat Slug as well as the terminal
4. Radius on terminal is Optional

INTERNATIONAL RECTIFIER LOGO

DATE CODE

ASSEMBLY SITE CODE

(Per SCOP 200-002)

PIN 1 IDENTIFIER

XXXX

XYWWX

XXXXX

PART NUMBER
("4 or 5 digits")

MARKING CODE
(Per Marking Spec)

LOT CODE
(Eng Mode - Min last 4 digits of EATI#)
(Prod Mode - 4 digits of SPN code)

REEL DIMENSIONS

Reel Diameter

Reel Width (W1)

TAPE DIMENSIONS

K_0

P_1

A_0

B_0

W

CODE	DESCRIPTION
A_0	Dimension design to accommodate the component width
B_0	Dimension design to accommodate the component length
K_0	Dimension design to accommodate the component thickness
W	Overall width of the carrier tape
P_1	Pitch between successive cavity centers

A diagram of a packet structure. At the top, a horizontal row of five circles is labeled "Sprocket Holes". Below this, a rectangular area contains two identical square grids. Each grid is divided into four quadrants, labeled "Q1", "Q2", "Q3", and "Q4" in a 2x2 arrangement. A label "Packet Quadrants" points to the bottom-left quadrant of the left grid. To the right of the grids is a large, solid black arrow pointing to the right, labeled "User Direction of Feed".

Package Type	Reel Diameter (Inch)	QTY	Reel Width W1 (mm)	Ao (mm)	Bo (mm)	Ko (mm)	P1 (mm)	W (mm)	Pin 1 Quadrant
5 X 6 P QFN	13	4000	12.4	6.300	5.300	1.20	8.00	12	Q1

8

Qualification information[†]

Qualification level	Consumer ^{††} (per JEDEC JESD47F ^{†††} guidelines)	
Moisture Sensitivity Level	PQFN 5mm x 6mm	MSL1 (per JEDEC J-STD-020D ^{†††})
RoHS compliant	Yes	

[†] Qualification standards can be found at International Rectifier's web site

<http://www.irf.com/product-info/reliability>

^{††} Higher qualification ratings may be available should the user have such requirements.

Please contact your International Rectifier sales representative for further information:

<http://www.irf.com/whoto-call/salesrep/>

^{†††} Applicable version of JEDEC standard at the time of product release.

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Starting $T_J = 25^{\circ}\text{C}$, $L = 0.47\text{mH}$, $R_G = 50\Omega$, $I_{AS} = 20\text{A}$.
- ③ Pulse width $\leq 400\mu\text{s}$; duty cycle $\leq 2\%$.
- ④ R_{θ} is measured at T_J of approximately 90°C .
- ⑤ When mounted on 1 inch square 2 oz copper pad on 1.5x1.5 in. board of FR-4 material.
- ⑥ Calculated continuous current based on maximum allowable junction temperature.
- ⑦ Current is limited to 50A by source bonding technology.

Revision History

Date	Comment
1/21/2014	- Updated ordering information to reflect the End-Of-Life (EOL) of the mini-reel option (EOL notice #259). - Updated data sheet with the new IR corporate template.
6/2/2015	- Updated package outline for "option E" and added package outline for "option G" on page 7 - Updated "IRFX" logo on page 1 & 9. - Updated tape and reel on page 8.

International
 Rectifier

AN INFINEON TECHNOLOGIES COMPANY

IR WORLD HEADQUARTERS: 101 N. Sepulveda Blvd., El Segundo, California 90245, USA

To contact International Rectifier, please visit <http://www.irf.com/whoto-call/>

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