

N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C		
50V	3.5Ω @ V _{GS} = 10V	200mA		

Description and Applications

This MOSFET has been designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

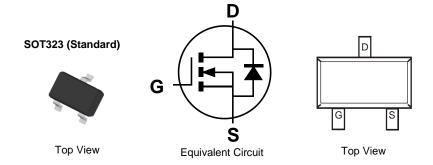
Load Switch

Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen- and Antimony-Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/
- An Automotive-Compliant Part is Available Under Separate Datasheet (<u>BSS138WQ</u>)

Mechanical Data

- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208@3
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)



Ordering Information (Note 4)

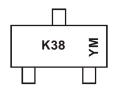
Part Number	Case	Packaging
BSS138W-7-F	SOT323 (Standard)	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



K38 = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: I = 2021) M or \overline{M} = Month (ex: 9 = September)

Date Code Key

Year	2003		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	Р			J	K	L	М	N	0	Р	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Maximum Ratings (@ T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage	VDSS	50	V	
Drain-Gate Voltage (Note 5)		Vdgr	50	V
Gate-Source Voltage	Continuous	V _{GSS}	±20	V
Drain Current (Note 6)	Continuous	ID	200	mA

Thermal Characteristics (@ TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	PD	200	mW
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	625	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

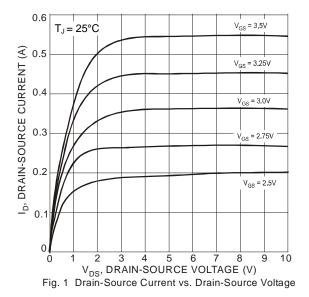
Electrical Characteristics (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BVDSS	50	75		V	$V_{GS} = 0V, I_{D} = 250\mu A$
Zero Gate Voltage Drain Current	IDSS	_	_	0.5	μΑ	$V_{DS} = 50V$, $V_{GS} = 0V$
Gate-Body Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	Vgs(TH)	0.5	1.2	1.5	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
Static Drain-Source On-Resistance	RDS(ON)	_	1.4	3.5	Ω	$V_{GS} = 10V, I_D = 0.22A$
Forward Transconductance	grs	100			mS	$V_{DS} = 25V$, $I_{D} = 0.2A$, $f = 1.0kHz$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	_	50	pF	
Output Capacitance	Coss			25	pF	$V_{DS} = 10V, V_{GS} = 0V, f = 1.0MHz$
Reverse Transfer Capacitance	Crss	_	_	8.0	pF	
Turn-On Delay Time	tD(ON)	_		20	ns	$V_{DD} = 30V$, $I_D = 0.2A$,
Turn-Off Delay Time	t _{D(OFF)}			20	ns	$R_{GEN} = 50\Omega$

 $Notes: \hspace{1cm} 5. \hspace{1cm} Rgs \leq 20 k\Omega.$

- 6. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Incorporated's suggested pad layout document, which can be found on our website at http://www.diodes.com/package-outlines.html.
- 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to production testing.





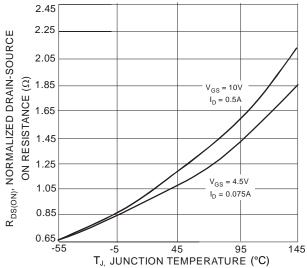


Fig. 3 Drain-Source On Resistance vs. Junction Temperature

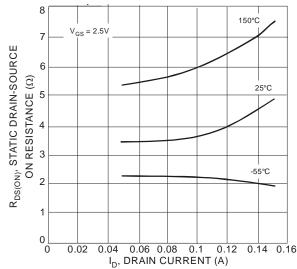
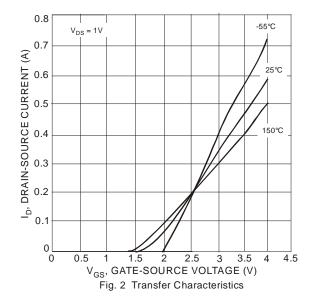
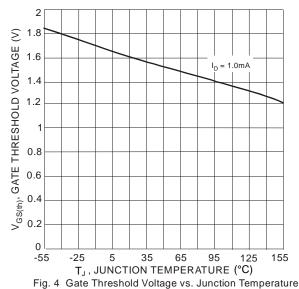


Fig. 5 Drain-Source On Resistance vs. Drain Current





9 V_{GS} = 2.75V 8 7 7 150°C

150°C

25°C

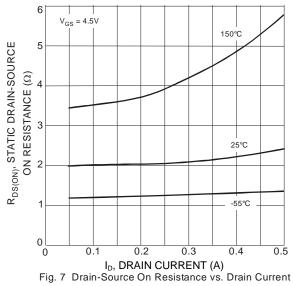
25°C

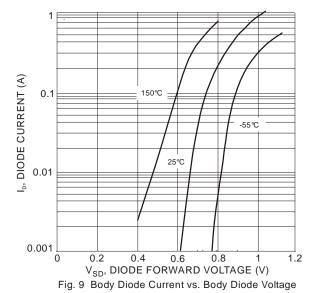
1 0 0 0.05 0.1 0.15 0.2 0.25

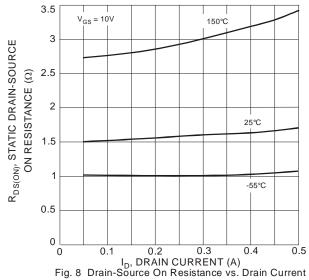
I_D, DRAIN CURRENT (A)

Fig. 6 Drain-Source On Resistance vs. Drain Current









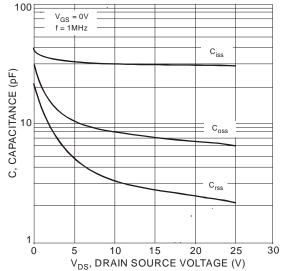
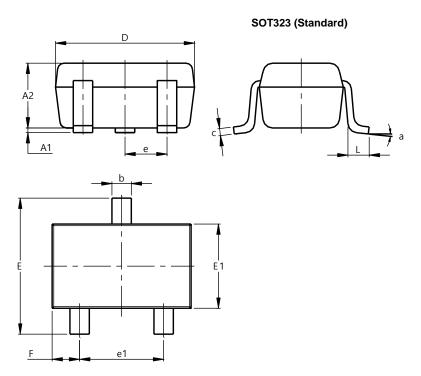


Fig. 10 Capacitance vs. Drain Source Voltage



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

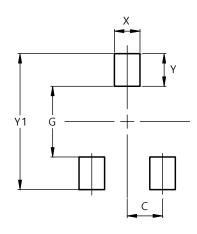


SOT323 (Standard)							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.80	1.00	0.90				
b	0.20	0.40	0.30				
С	0.08	0.18	0.13				
D	1.80	2.20	2.00				
Е	2.00	2.45	2.225				
E1	1.15	1.35	1.25				
е			0.65				
e1	1.20	1.40	1.30				
F	0.25	0.475	0.3625				
L	0.25	0.46	0.355				
а	0°	8°					
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323 (Standard)



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.470
Y	0.600
Y1	2.500



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