EMH9 / UMH9N / IMH9A

General purpose (dual digital transistors)

Parameter	DTr1 and DTr2
V _{CC}	50V
I _{C(MAX.)}	100mA
R ₁	10kΩ
R ₂	47kΩ

Features

- 1)Two DTC114Y chips in a EMT or UMT or SMT package.
- 2)Mounting possible with EMT3 or UMT3 or SMT3 automatic mounting machines.
- 3)Transistor elements are independent, eliminating interference.
- 4)Mounting cost and area can be cut in half.



Inner circuit



IMH9A



• Application INVERTER, INTERFACE, DRIVER

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
EMH9	SOT-563 (EMT6)	1616	T2R	180	8	8000	H9
UMH9N	SOT-363 (UMT6)	2021	TN	180	8	3000	H9
IMH9A	SOT-457 (SMT6)	2928	T110	180	8	3000	H9

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● Absolute maximum ratings (T_a = 25°C)

<For DTr1 and DTr2 in common>

Parameter		Symbol	Values	Unit
Supply voltage		V _{CC}	50	V
Input voltage		V _{IN}	-6 to 40	V
Output current		Ι _Ο	70	mA
Collector current		I _{C(MAX)} *1	100	mA
	EMH9	P _D ^{*2*3}	150	
Power dissipation	UMH9N	P _D *2*3	150	mW
	IMH9A	P _D *2*4	300	
Junction temperature		Tj	150	°C
Range of storage temperature		T _{stg}	-55 to +150	°C

• Electrical characteristics (T_a = 25°C)

<For DTr1 and DTr2 in common>

Deremeter	Queen al	Conditions	Values			1.1
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
	V _{I(off)}	V _{CC} = 5V, I _O = 100µA	-	-	0.3	
Input voltage	V _{I(on)}	V _O = 0.3V, I _O = 1mA	1.4	-	-	V
Output voltage	V _{O(on)}	I _O = 5mA, I _I = 0.25mA	-	100	300	mV
Input current	I _I	V _I = 5V	-	-	880	μA
Output current	I _{O(off)}	V _{CC} = 50V, V _I = 0V	-	-	500	nA
DC current gain	G _I	V _O = 5V, I _O = 5mA	68	-	-	-
Input resistance	R ₁	-	7	10	13	kΩ
Resistance ratio	R_2/R_1	-	3.7	4.7	5.7	-
Transition frequency	f _T *1	V _{CE} = 10V, I _E = -5mA, f = 100MHz	-	250	-	MHz

*1 Characteristics of built-in transistor

*2 Each terminal mounted on a reference land.

*3 120mW per element must not be exceeded.

*4 200mW per element must not be exceeded.



•Electrical characteristic curves (T_a = 25°C) <For DTr1 and DTr2 in common>







Fig.2 Output Current vs. Input Voltage (OFF Characteristics)

 $\textbf{INPUT VOLTAGE}: V_{l(off)} \text{ [V]}$

Fig.3 Output Current vs. Output Voltage



Fig.4 DC Current Gain vs. Output Current



DC CURRENT GAIN : G

●Electrical characteristic curves (T_a = 25°C)

<For DTr1 and DTr2 in common>



Fig.5 Output Voltage vs. Output Current



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
A	0.45	0.55	0.018	0.022
A1	0.00	0.10	0.000	0.004
b	0.17	0.27	0.007	0.011
с	0.08	0.18	0.003	0.007
D	1.50	1.70	0.059	0.067
E	1.10	1.30	0.043	0.051
е	0.50		0.0	20
HE	1.50	1.70	0.059	0.067
L	0.10	0.30	0.004	0.012
Lp	-	0.35	-	0.014
x	-	0.10	-	0.004
У		0.10	-	0.004
DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
b2	-	0.37	-	0.015
e1	1.	25	0.049	
11	-	0.45	-	0.018

Dimension in mm/inches



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
	MIN	MAX	MIN	MAX
A	0.80	1.00	0.031	0.039
A1	0.00	0.10	0.000	0.004
A3	0.	25	0.0	10
b	0.15	0.30	0.006	0.012
с	0.10	0.20	0.004	0.008
D	1.90	2.10	0.075	0.083
E	1.15	1.35	0.045	0.053
е	0.	65	0.026	
HE	2.00	2.20	0.079	0.087
L1	0.20	0.50	0.008	0.020
Lp	0.25	0.55	0.010	0.022
Q	0.10	0.30	0.004	0.012
х	-	0.10	-	0.004
У	 3	0.10	-	0.004
DIM	MILIM	ETERS	INC	HES
	MIN	MAX	MIN	MAX

		ETERS	INCHES	
DIN	MIN	MAX	MIN	MAX
b2	-	0.40	-	0.016
e1	1.55		0.0	61
1	-	0.65	-	0.026

Dimension in mm/inches



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	MILIMETERS		HES
DIN	MIN	MAX	MIN	MAX
А	1.00	1.30	0.039	0.051
A1	0.00	0.10	0.000	0.004
A3	0.	25	0.0	10
b	0.25	0.40	0.010	0.016
с	0.09	0.25	0.004	0.010
D	2.80	3.00	0.110	0.118
E	1.50	1.80	0.059	0.071
е	0.	95	0.037	
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.20	0.30	0.008	0.012
х	-	0.20	-	0.008
У	-	0.10	-	0.004

DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
b2		0.60	-	0.024
e1	2.10		0.0	83
11	-	0.90		0.035

Dimension in mm/inches



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