

Features

- High Density Cell Desihn for Ultra Low R_{DS(on)}
- · Fully Characterized Avalanche Voltage and Current
- Good Stability and Uniformity with High E_{AS}
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

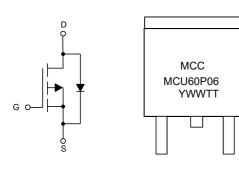
Maximum Ratings

- Operating Junction Temperature Range : -55°C to +175°C
- Storage Temperature Range: -55°C to +175°C
- Thermal Resistance: 0.847°C/W Junction to Case

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V _{DS}	-60	V
Gate-Source Volltage		V _{GS}	±20	V
Continuous Drain Current	T _C =25°C	1	-60	Α
	T _C =100°C	- I _D	-42.3	Α
Pulsed Drain Current		I _{DM}	-260	Α
Single Pulse Avalanche Energy (Note 2)		E _{AS}	722	mJ
Total Power Dissipation		P _D	177	W

Note:

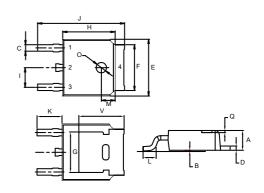
Internal Structure and Marking Code



YWWTT: 5 codes in total Y is the year WW is the cycle TT is the line type

P-CHANNEL MOSFET

DPAK(TO-252)



- 1. Gate
- 2,4. Drain
- 3. Source

DIMENSIONS					
DIM	INCHES		MM		NOTE
ווועו	MIN	MAX	MIN	MAX	NOTE
Α	0.087	0.094	2.20	2.40	
В	0.000	0.005	0.00	0.13	
С	0.026	0.034	0.66	0.86	
D	0.018	0.023	0.46	0.58	
Ε	0.256	0.264	6.50	6.70	
F	0.201	0.215	5.10	5.46	
G	0.190		4.83		TYP.
Н	0.236	0.244	6.00	6.20	
I	0.086	0.094	2.18	2.39	
J	0.386	0.409	9.80	10.40	
K	0.1	14	2.9	90	TYP.
L	0.055	0.067	1.40	1.70	
М	0.063		1.60		TYP.
0	0.043	0.051	1.10	1.30	
Q	0.000	0.012	0.00	0.30	
V	0.211		5.35		TYP.

^{1.} Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

 $^{2.}T_J = 25$ °C, $V_{DD} = -30$ V, $V_{GS} = -10$ V,L = 0.5mH, $R_q = 25$ Ω.



Electrical Characteristics @ 25°C (Unless Otherwise Specified)

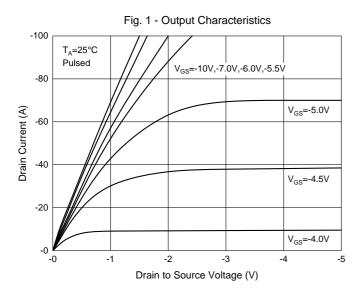
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =-250µA	-60			V
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-60V, V _{GS} =0V			-1	μA
Gate-Threshold Voltage ^(Note 3)	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-2	-2.6	-3.5	V
Drain-Source On-Resistance ^(Note 3)	R _{DS(on)}	V _{GS} =-10V, I _D =-20A		13	18	mΩ
Forward Tranconductance ^(Note 3)	9 FS	V _{DS} =-5V, I _D =-20A		25		S
Dynamic Characteristics(Note 4)				•		
Input Capacitance	C _{iss}			5814		
Output Capacitance	C _{oss}	V _{DS} =-25V,V _{GS} =0V,f=1MHz		483		pF
Reverse Transfer Capacitance	C _{rss}			234		
Total Gate Charge	Q_g			75		
Gate-Source Charge	Q_{gs}	V _{DS} =-30V,V _{GS} =-10V,I _D =-20A		16		nC
Gate-Drain Charge	Q_{gd}			19		
Reverse Recovery Chrage	Q _{rr}	I _s =-20A, di/dt=-100A/µs		71		
Reverse Recovery Time	t _{rr}	1 _S 20A, αι/αι100A/μS		49		
Turn-On Delay Time	t _{d(on)}			18		
Turn-On Rise Time	t _r	V_{DD} =-30V, R_{L} =1.5 Ω ,		20		ns
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10V, R_{G} =3 Ω		55		
Turn-Off Fall Time	t _f			35		
Drain-Source Body Diode Cha	racteristi	cs	•	•		
Continuous Body Diode Current	Is	T _C =25°C			-60	Α
Body Diode Voltage	V _{SD}	I _{SD} =-20A, V _{GS} =0V			-1.2	V

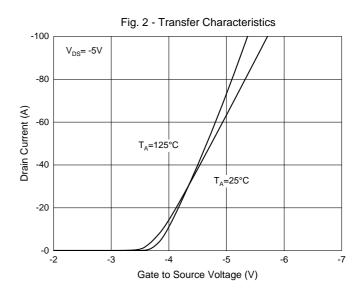
Note 3. Pulse Test : Pulse Width≤300µs, Duty Cycle ≤2%.

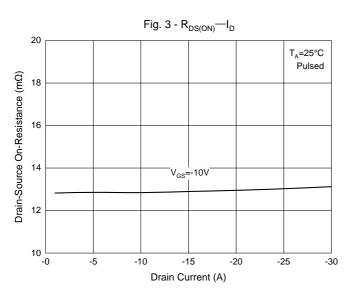
4. Guaranteed by Design, Not Subject to Production Testing.

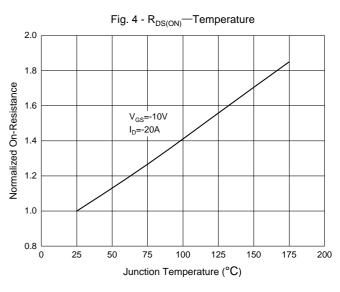


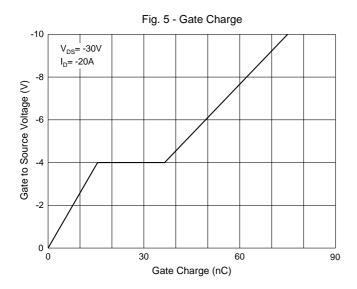
Curve Characteristics

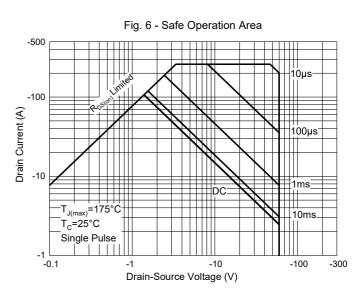














Curve Characteristics

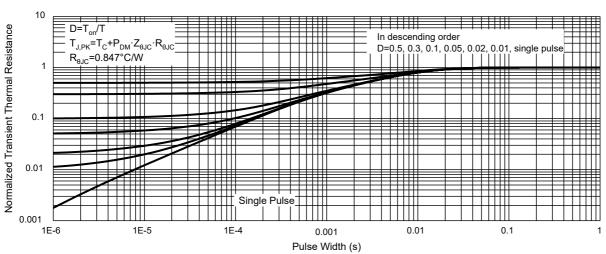


Fig. 7 - Normalized Transient Thermal Impedance

Rev.3-5-08182023 4/5 MCCSEMI.COM



Ordering Information

Device	Packing	
Part Number-TP	Tape&Reel: 2.5Kpcs/Reel	

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