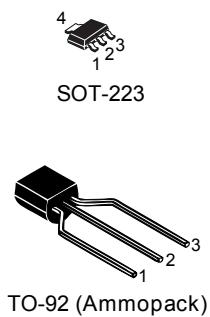


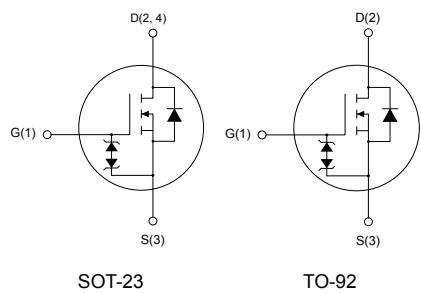
N-channel 800 V, 13 Ω typ., 250 mA SuperMESH Power MOSFETs in a SOT-223 and TO-92 packages



Features

Order codes	V _{DS}	R _{DS(on)} max.	I _D
STN1NK80Z	800 V	16 Ω	250 mA
STQ1NK80ZR-AP			

- 100% avalanche tested
- Gate charge minimized
- Very low intrinsic capacitance
- Zener-protected



Applications

- Switching applications

Description

These high-voltage devices are Zener-protected N-channel Power MOSFETs developed using the SuperMESH technology by STMicroelectronics, an optimization of the well-established PowerMESH. In addition to a significant reduction in on-resistance, these devices are designed to ensure a high level of dv/dt capability for the most demanding applications.



Product status links	
STN1NK80Z	
STQ1NK80ZR-AP	

Product summary	
Order code	STN1NK80Z
Marking	N1NK80Z
Package	SOT-223
Packing	Tape and reel
Order code	STQ1NK80ZR-AP
Marking	Q1NK80ZR
Package	TO-92
Packing	Ammopack

1 Electrical ratings

Table 1. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{DS}	Drain-source voltage	800	V
V _{GS}	Gate-source voltage	±30	V
I _D	Drain current (continuous) at T _C = 25 °C	0.25	A
	Drain current (continuous) at T _C = 100 °C	0.16	
I _{DM} ⁽¹⁾	Drain current (pulsed)	5	A
P _{TOT}	Total power dissipation at T _C = 25 °C	2.5	W
ESD	Gate-source human body model (R = 1.5 kΩ, C = 100 pF)	1	kV
dv/dt ⁽²⁾	Peak diode recovery voltage slope	4.5	V/ns
T _{stg}	Storage temperature range	-55 to 150	°C
T _J	Operating junction temperature range		°C

1. Pulse width limited by safe operating area.
2. I_{SD} ≤ 1 A, di/dt ≤ 200 A/μs, V_{DS} (peak) ≤ V_{(BR)DSS}, V_{DD} = 80% V_{(BR)DSS}.

Table 2. Thermal data

Symbol	Parameter	Value	Unit
R _{thJA}	Thermal resistance, junction-to-ambient	50	°C/W

Table 3. Avalanche characteristics

Symbol	Parameter	Value	Unit
I _{AR}	Avalanche current, repetitive or not repetitive (pulse width limited by T _J max.)	1	A
E _{AS}	Single pulse avalanche energy (starting T _J = 25 °C, I _D = I _{AR} , V _{DD} = 50 V)	50	mJ

2 Electrical characteristics

$T_C = 25^\circ\text{C}$ unless otherwise specified.

Table 4. On/off-state

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$V_{(\text{BR})\text{DSS}}$	Drain-source breakdown voltage	$V_{GS} = 0 \text{ V}, I_D = 1 \text{ mA}$	800			V
I_{DSS}	Zero gate voltage drain current	$V_{GS} = 0 \text{ V}, V_{DS} = 800 \text{ V}$			1	μA
		$V_{GS} = 0 \text{ V}, V_{DS} = 800 \text{ V}, T_C = 125^\circ\text{C}$ ⁽¹⁾			50	
I_{GSS}	Gate body leakage current	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 10	μA
$V_{GS(\text{th})}$	Gate threshold voltage	$V_{DS} = V_{GS}, I_D = 50 \mu\text{A}$	3.0	3.8	4.5	V
$R_{\text{DS(on)}}$	Static drain-source on-resistance	$V_{GS} = 10 \text{ V}, I_D = 0.5 \text{ A}$		13	16	Ω

1. Specified by design, not tested in production.

Table 5. Dynamic

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
C_{iss}	Input capacitance		-	160	-	pF
C_{oss}	Output capacitance	$V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}, V_{GS} = 0 \text{ V}$	-	26	-	pF
C_{rss}	Reverse transfer capacitance		-	6.7	-	pF
$C_{oss \text{ eq.}}^{(1)}$	Equivalent output capacitance	$V_{GS} = 0 \text{ V}, V_{DS} = 0 \text{ to } 640 \text{ V}$	-	9.5	-	pF
Q_g	Total gate charge		-	7.7	-	nC
Q_{gs}	Gate-source charge	$V_{DD} = 640 \text{ V}, I_D = 1.1 \text{ A}, V_{GS} = 0 \text{ to } 10 \text{ V}$ (see Figure 14. Test circuit for gate charge behavior)	-	1.4	-	nC
Q_{gd}	Gate-drain charge		-	4.5	-	nC

1. $C_{oss \text{ eq.}}$ is defined as the constant equivalent capacitance giving the same charging time as C_{oss} when V_{DS} increases from 0 to 80% V_{DSS}

Table 6. Switching times

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on delay time	$V_{DD} = 400 \text{ V}, I_D = 0.55 \text{ A}, R_G = 4.7 \Omega, V_{GS} = 10 \text{ V}$	-	7	-	ns
t_r	Rise time		-	23	-	ns
$t_{d(off)}$	Turn-off delay time	see (Figure 13. Test circuit for resistive load switching times and Figure 18. Switching time waveform)	-	18	-	ns
t_f	Fall time		-	28	-	ns

Table 7. Source-drain diode

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
I_{SD}	Source-drain current		-		1	A
$I_{SDM}^{(1)}$	Source-drain current (pulsed)		-		5	A
$V_{SD}^{(2)}$	Forward on voltage	$I_{SD} = 1 \text{ A}, V_{GS} = 0 \text{ V}$	-		1.6	V
t_{rr}	Reverse recovery time	$I_{SD} = 1.1 \text{ A}, dI/dt = 100 \text{ A}/\mu\text{s},$ $V_{DD} = 50 \text{ V}$	-	365		ns
Q_{rr}	Reverse recovery charge	(see Figure 15. Test circuit for inductive load switching and diode recovery times)	-	802		nC
I_{RRM}	Reverse recovery current		-	4.4		A
t_{rr}	Reverse recovery time	$I_{SD} = 1.1 \text{ A}, dI/dt = 100 \text{ A}/\mu\text{s},$ $V_{DD} = 50 \text{ V}, T_J = 150 \text{ }^\circ\text{C}$	-	389		ns
Q_{rr}	Reverse recovery charge	(see Figure 15. Test circuit for inductive load switching and diode recovery times)	-	891		nC
I_{RRM}	Reverse recovery current		-	4.6		A

1. Pulse width limited by safe operating area.
2. Pulsed: pulse duration = 300 μs , duty cycle 1.5%.

2.1 Electrical characteristics (curves)

Figure 1. Safe operating area

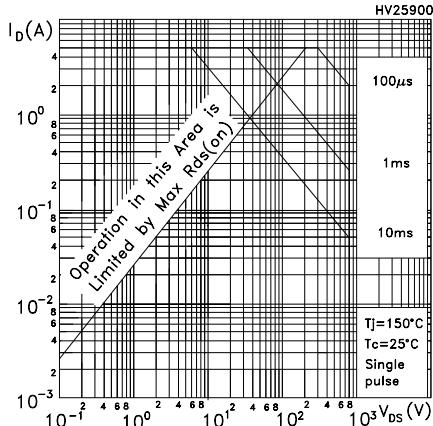


Figure 2. Normalized transient thermal impedance

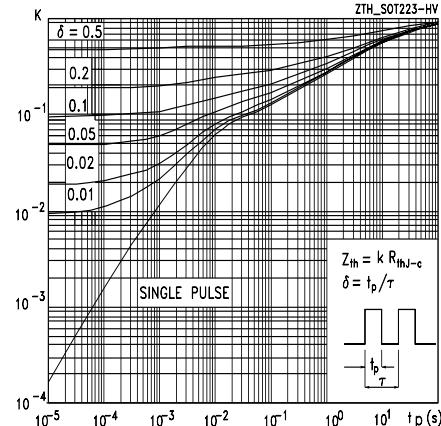


Figure 3. Typical output characteristics

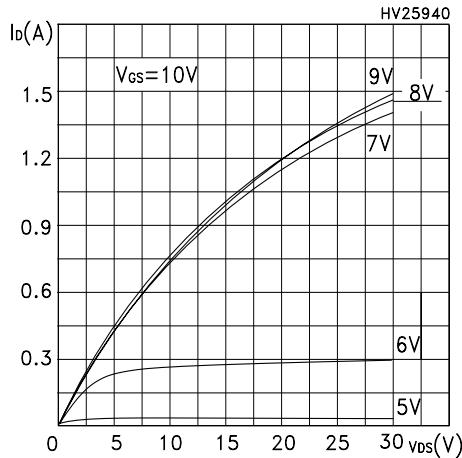


Figure 4. Typical transfer characteristics

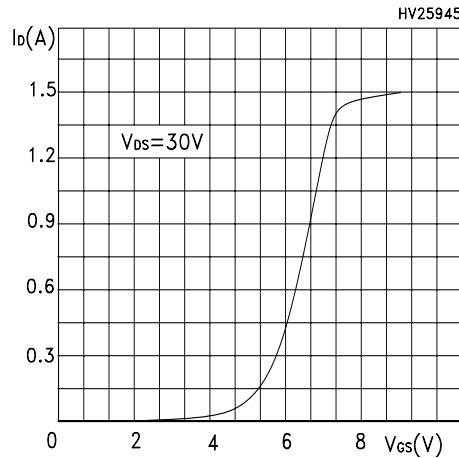


Figure 5. Typical drain-source on-resistance

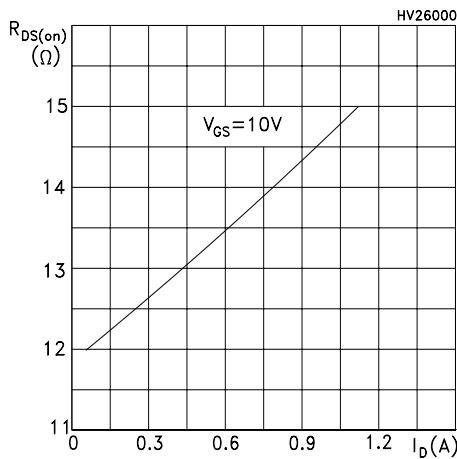


Figure 6. Typical gate charge characteristics

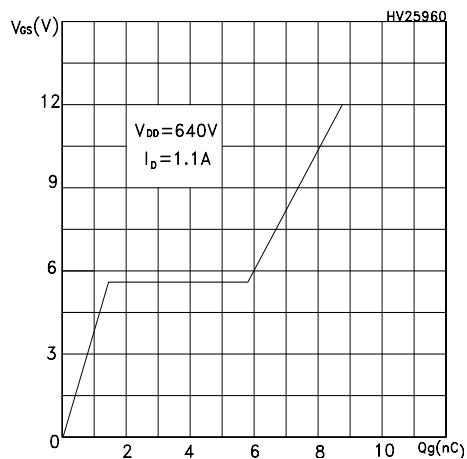
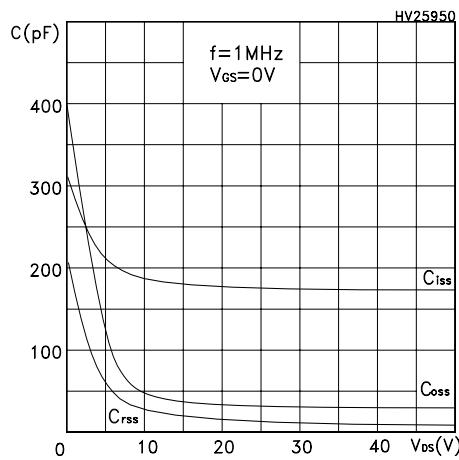
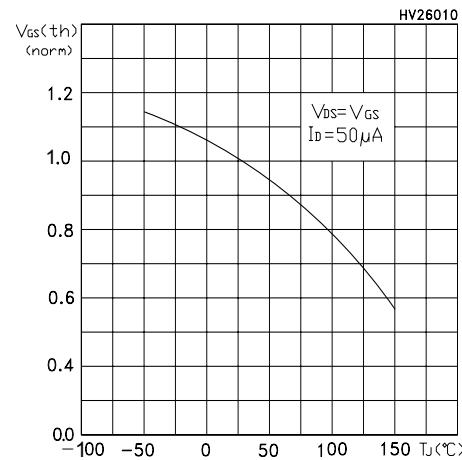
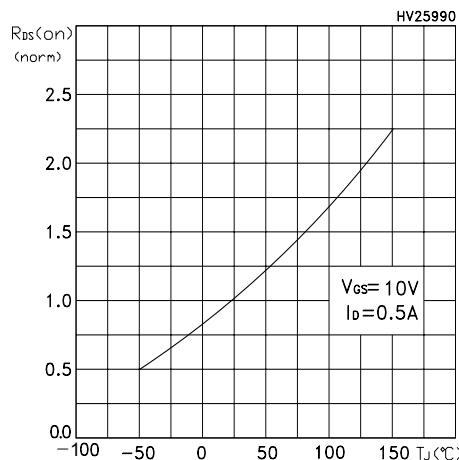
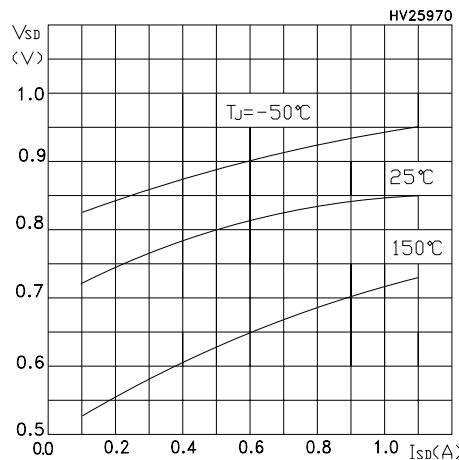
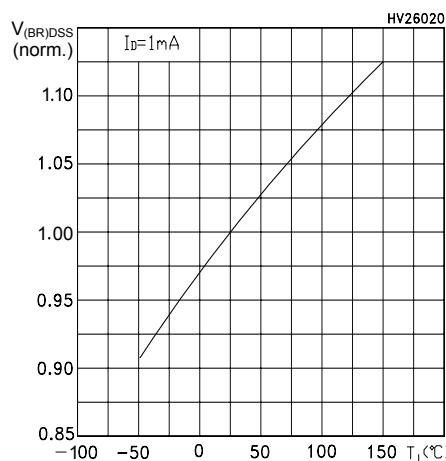
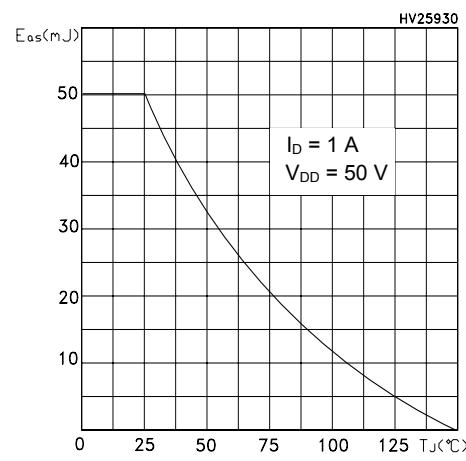
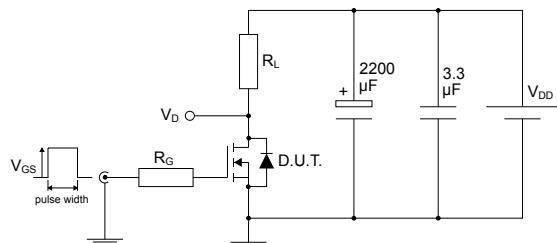


Figure 7. Typical capacitance characteristics

Figure 8. Normalized gate threshold vs temperature

Figure 9. Normalized on-resistance vs temperature

Figure 10. Typical reverse diode forward characteristics

Figure 11. Normalized breakdown voltage vs temperature

Figure 12. Maximum avalanche energy vs temperature


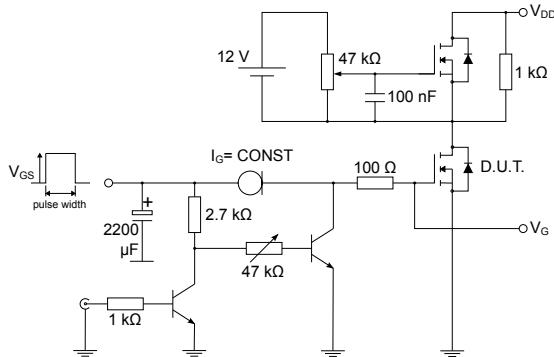
3 Test circuits

Figure 13. Test circuit for resistive load switching times



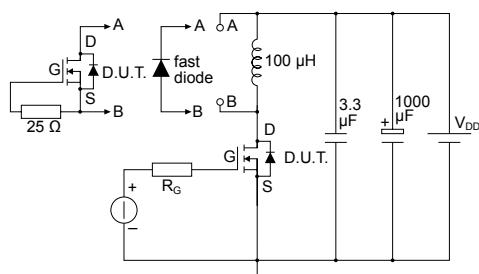
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Figure 14. Test circuit for gate charge behavior



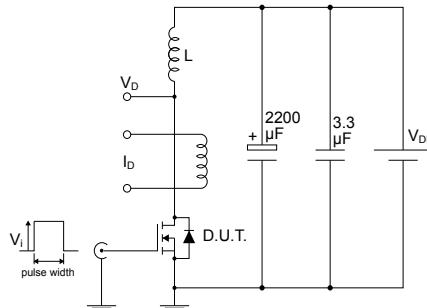
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Figure 15. Test circuit for inductive load switching and diode recovery times



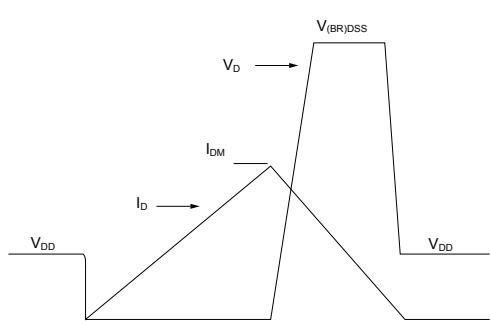
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Figure 16. Unclamped inductive load test circuit



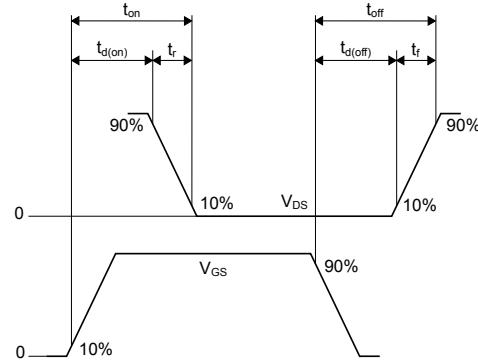
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Figure 17. Unclamped inductive waveform



AM01472v1

Figure 18. Switching time waveform



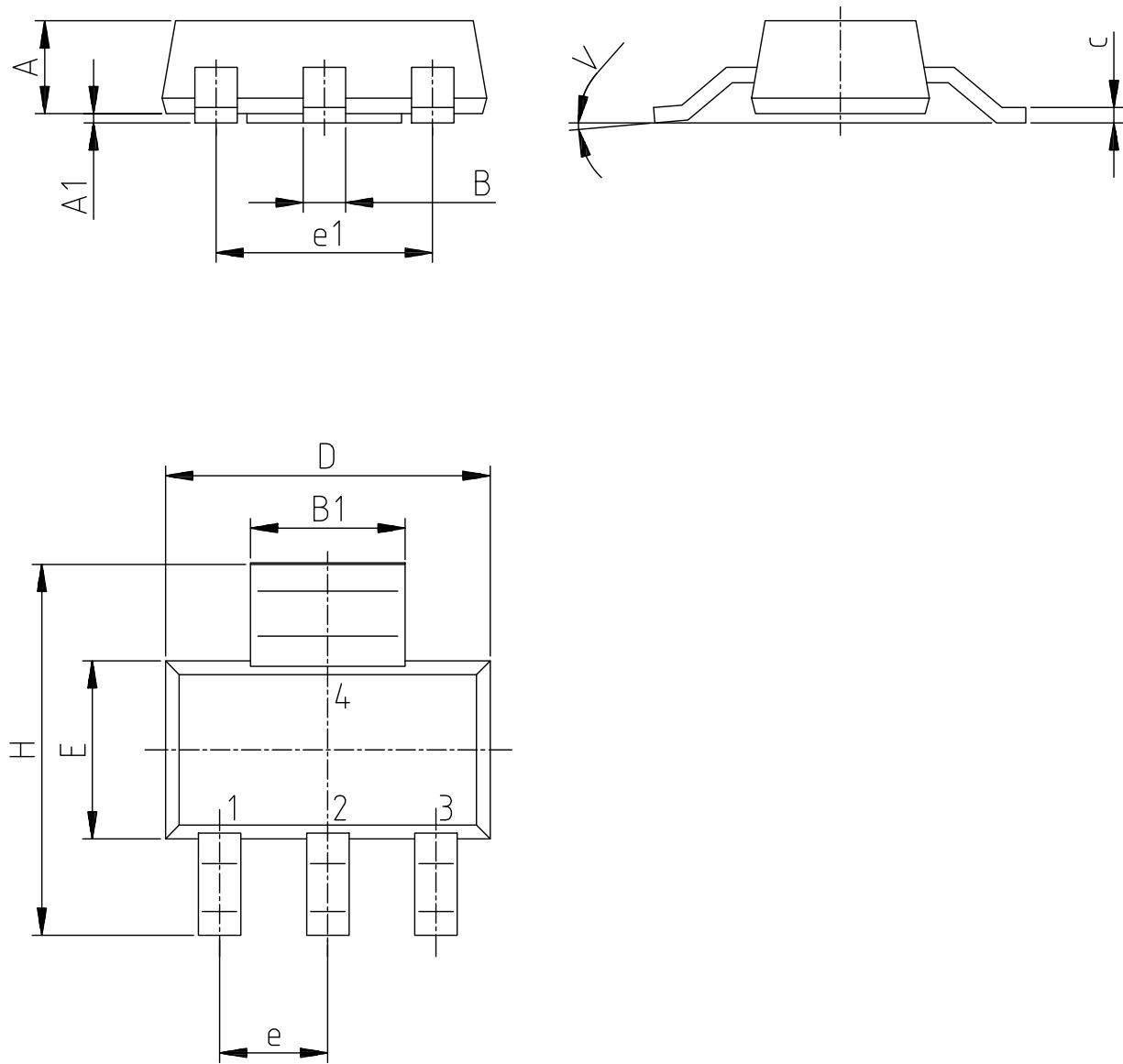
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4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

4.1 SOT-223 package information

Figure 19. SOT-223 package outline

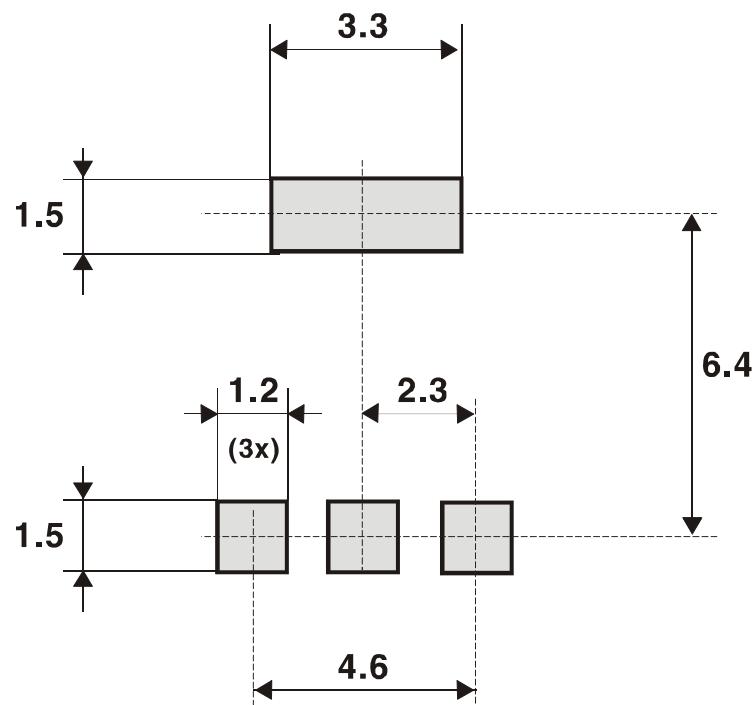


0046067_15

Table 8. SOT-223 package mechanical data

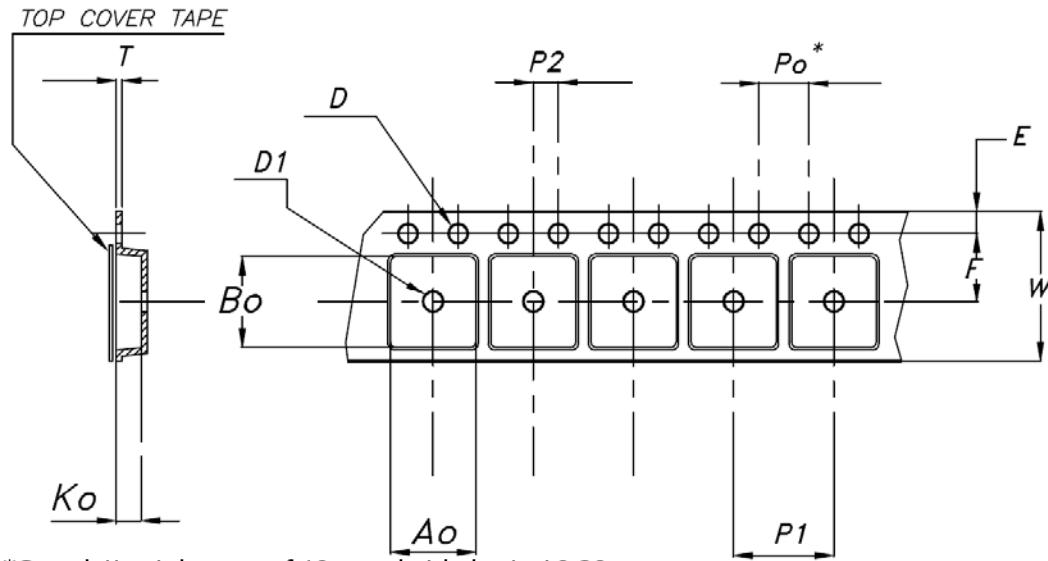
Dim.	mm		
	Min.	Typ.	Max.
A			1.8
B	0.6	0.7	0.85
B1	2.9	3	3.15
c	0.24	0.26	0.35
D	6.3	6.5	6.7
e		2.3	
e1		4.6	
E	3.3	3.5	3.7
H	6.7	7	7.3
V			10 deg
A1	0.02		0.1

Figure 20. SOT-223 recommended footprint (dimensions are in mm)



4.2 SOT-223 packing information

Figure 21. SOT-223 tape outline



*Cumulative tolerance of 10 sprocket holes is $\pm 0.20 \text{ mm}$

Figure 22. SOT-223 reel outline

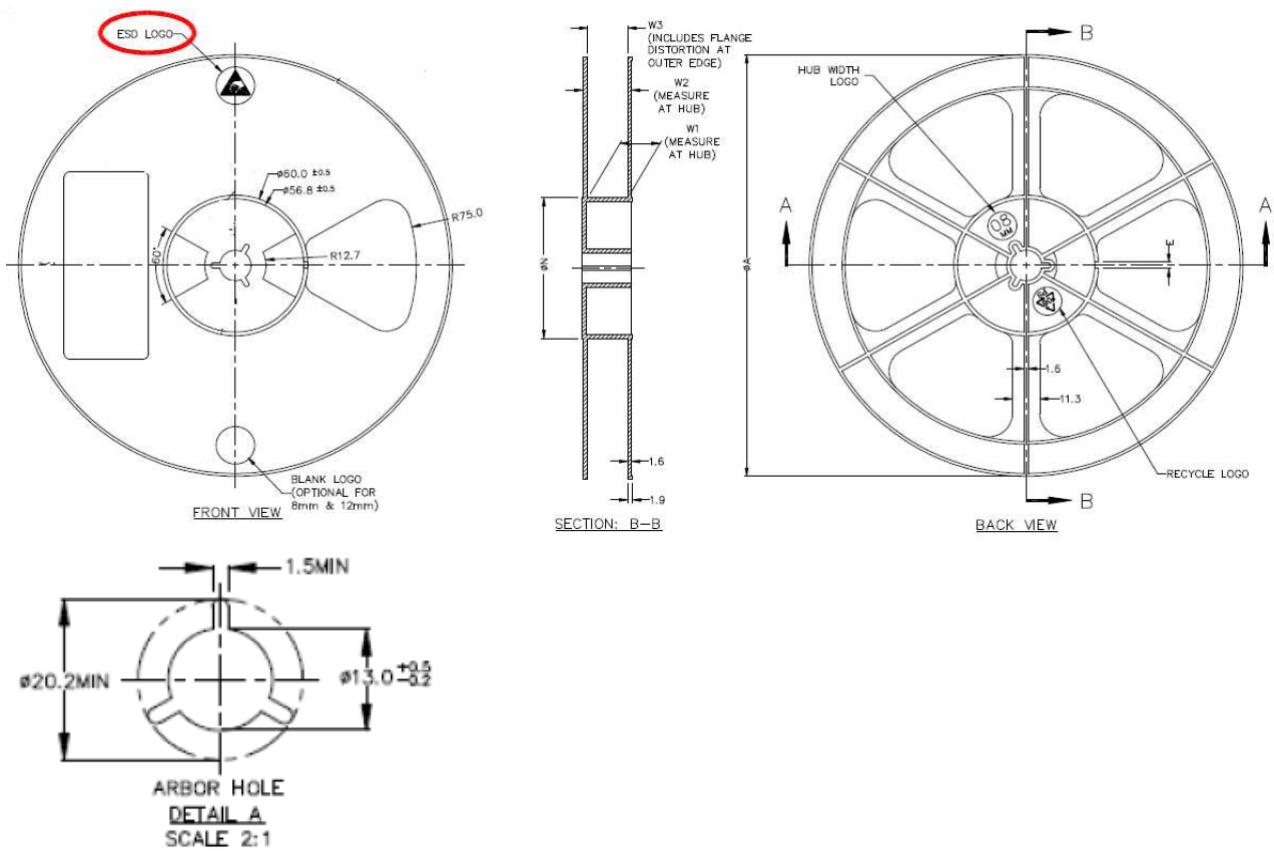
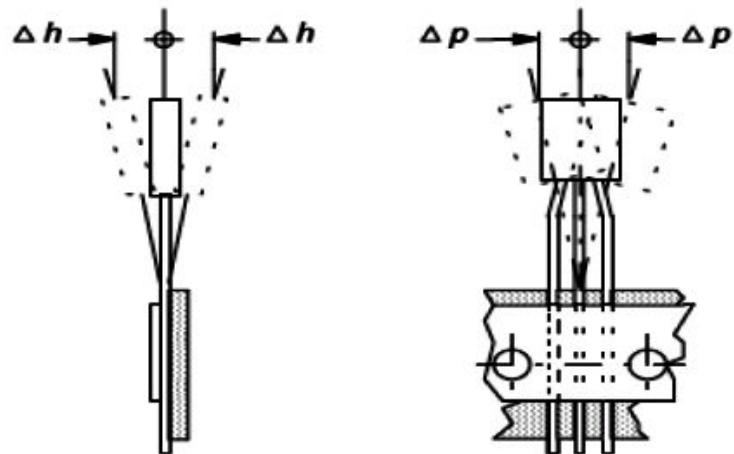
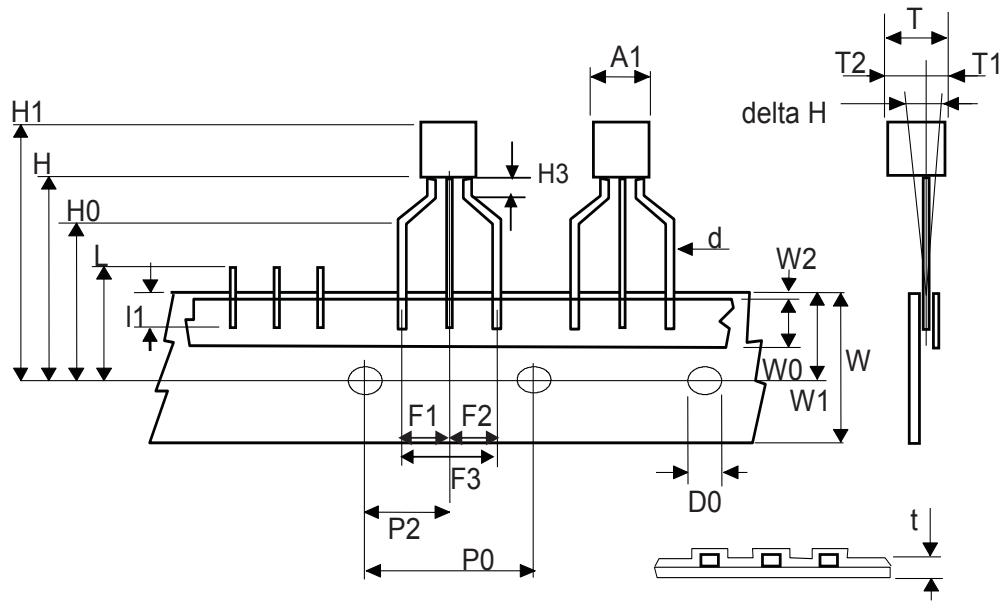


Table 9. SOT-223 tape and reel mechanical data

Tape				Tape		
Dim.	mm			Dim.	mm	
	Min.	Typ.	Max.		Min.	Max.
A0	6.75	6.85	6.95	A		180
B0	7.30	7.40	7.50	N	60	
K0	1.80	1.90	2.00	W1		12.4
F	5.40	5.50	5.60	W2		18.4
E	1.65	1.75	1.85	W3	11.9	15.4
W	11.7	12.0	12.3			
P2	1.90	2.00	2.10	Base quantity pcs		1000
P0	3.90	4.00	4.10	Bulk quantity pcs		1000
P1	7.90	8.00	8.10			
T	0.25	0.30	0.35			
DΦ	1.50	1.55	1.60			
D1Φ	1.50	1.60	1.70			

4.3 TO-92 ammopack package information

Figure 23. TO-92 ammopack package outline



0050910_Rev_22

Table 10. TO-92 ammopack mechanical data

Dim.	mm		
	Min.	Typ.	Max.
A1			4.80
T			3.80
T1			1.60
T2			2.30
d	0.45	0.47	0.48
P0	12.50	12.70	12.90
P2	5.65	6.35	7.05
F1, F2	2.40	2.50	2.94
F3	4.98	5.08	5.48
delta H	-2.00		2.00
W	17.50	18.00	19.00
W0	5.50	6.00	6.50
W1	8.50	9.00	9.25
W2			0.50
H		18.50	21.00
H0	15.50	16.00	18.20
H1		25.00	27.00
H3	0.50	1.00	2.00
D0	3.80	4.00	4.20
t			0.90
L			11.00
I1	3.00		
delta P	-1.00		1.00

Revision history

Table 11. Document revision history

Date	Revision	Changes
15-Mar-2023	1	First release. Part numbers previously included in datasheet DS4318

Contents

1	Electrical ratings	2
2	Electrical characteristics.....	3
2.1	Electrical characteristics (curves)	5
3	Test circuits	7
4	Package information.....	8
4.1	SOT-223 package information.....	8
4.2	SOT-223 packing information	10
4.3	TO-92 ammopack package information	12
	Revision history	14

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